Docket No.: SIA-P046

RECORDATION COVER SI

The Assistant Commissioner for Patents Washington, D.C. 20231

03-15-2001



101636161

Dear Sir:

Please record the attached original documents or copy thereof.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1. Name of conveying party:

Inventor (s): KIM, TAE HYOUNG KENGERI, SUBRAMANI

Serial No: (Not yet assigned)

Filed:

Execution Date:

<u>__2/16/20</u>01 2/16/2001

Group Art Unit:

(Not yet assigned)

Examiner:

(Not yet assigned)

2. Name and Address of

Receiving party:

Silicon Access Networks, Inc. 211 River Oaks Parkway

San Jose, CA 95134

3. Nature of Conveyance: Assignment

4. Patent Application(s) / Patent Number(s): (not yet assigned)

New Patent Application Entitled:

"LOADLESS NMOS FOUR TRANSISTOR SRAM CELL"

5. Name and address of party to whom correspondences concerning document should be mailed:

Name:

DENNIS S. FERNANDEZ, ESQ.

Address:

FERNANDEZ & ASSOCIATES, LLP

PATENT ATTORNEYS

PO BOX D

MENLO PARK, CA 94026-6204

PHONE: (650) 325-4999 FAX: (650) 325-1203

EMAIL: iploft@iploft.com

6. Total Number of applications and patents involved: ONE

7. Fee Calculation

\$40.00 Assignment Recordation Fee

Total Fees (37 CFR 3.41):

\$40.00

X Enclosed

Authorized to be charged to Deposit Account

8. Deposit Account Number:

In case Applicant inadvertently miscalculated any required fee, the Commissioner is hereby authorized to charge the necessary additional amount associated with this communication or credit any overpayment to Deposit Account No: 500482. A duplicate copy of this authorization is enclosed.

9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

DENNIS S. FERNANDEZ, ESQ.

Reg. No. 34,160

Date: 2-23-2001

PATENT

00000129 09793872

REEL: 011578 FRAME: 0355

Docket No.: SIA-P046

ASSIGNMENT

Whereas, We, Tae Hyoung Kim and Subramani Kengeri, having agreed to assign the below identified invention to SILICON ACCESS NETWORKS, INC., ("SILICON ACCESS"), a <u>Delaware</u> corporation having its principal place of business at 211 River Oaks Parkway, San Jose, CA 95134, for good and valuable consideration, receipt of which is hereby acknowledged, I do hereby sell, assign, and transfer unto SILICON ACCESS, its successors and assigns, the entire right, title, and interest, including the right of priority in, to and under an application for Letters Patents of the United States entitled:

LOADLESS NMOS FOUR TRANSISTOR SRAM CELL

X filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or		
Serial No.:	, filed on	and the inventions set
forth and described therein, a thereto which may be granted t	nd any and all Letters Patent of t	he United States and of countries foreign
assigns, to execute and del application, whether original, or desirable fully to secure to said application and all patent	iver without further compensation continuation, divisional, or reissue SILICON ACCESS, its successorights therein, in the United States resecution of interference proceed	t of SILICON ACCESS, its successors or on any power of attorney, assignment, e, or other papers which may be necessary rs and assigns, the inventions described in and in any country foreign thereto, and to ings involving said inventions and in the
Inventor's Signature:	Tae Houng &	Date: 2-16-0/
Inventor's Signature:	, Off	Date: 2-16-07_

RECORDED: 02/23/2001

PATENT REEL: 011578 FRAME: 0356