FORM PTO-1595 ~ 1-31-92

03-15-2001



U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks. 1 Masse	101637962
Name of conveying party(ies):	Name and address of receiving party(ies):
Jonathan F. Churchill Jeffrey D. Kooiman Cathal G. Phelan Ashish S. Pancholy	Name: Cypress Semiconductor Corporation Internal Address:
Gary A. Gibbs  Additional name(s) of conveying party(ies) attached?   Yes	Street Address: 3939 N. First St.
	City San Jose State CA ZIP 95134  Additional name(s) & address(es) attached? ☐ Yes ☒ No
3. Nature of Conveyance:	
Execution Date: February 19, 2001	- 09/792/02
4. Application number(s) or patent number(s):	
If this document is being filed together with a new application	n, the execution date of this application is: February 19, 2001
A. Patent Application No.(s) B. Patent No.(s)	Additional numbers attached?  Yes  No
5. Name and address of party to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved:1
Name: Kevin L. Daffer	
Internal Address: Conley, Rose & Tayon, P.C.	
Street Address: P.O. Box 398	
City <u>Austin</u> State <u>TX</u> ZIP <u>78767-0398</u>	
	7. Total fee (37 CFR 3.41):\$ 40.00  ☐ Enclosed  ☐ Authorized to be charged to deposit account
	8. Deposit account number: 50-1505/5298-04600* (Attach a duplicate copy of this page if paying by deposit account)
	DO NOT USE THIS SPACE
9. Statement and Signature.	
To the best of my knowledge and belief, the foregoing inform	nation is fug and correct and any attached copy is a true copy of the original document.
Kevin L. Daffer  Name of Person Signing	February 23, 2001 Signature Date
Reg. No. 34,146	Total number of pages comprising cover sheet:
OMB No. 0651-011 (exp.4/94)	Total named of pages comprising corta since.
03/14/2001 GTON11 00000200 501505 09792102	
01 FC:581 40.00 CH	

\* If this account is found to have insufficient funds, the Commissioner is authorized to charge Conley, Rose & Tayon deposit account no. 50-1623/5298-04600.

**REEL: 011580 FRAME: 0640** 

## **ASSIGNMENT**

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned, does hereby:

SELL, ASSIGN AND TRANSFER to <u>CYPRESS SEMICONDUCTOR</u> <u>CORPORATION</u> (the "Assignee"), having a place of business at <u>3939 North First Street</u>, <u>San Jose</u>, <u>CA</u>, <u>95134</u> the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith, and is entitled "<u>DIRECT BIT LINE-BIT LINE DEFECT DETECTION TEST MODE FOR SRAM</u>", such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee; and

COVENANT AND AGREE that the above is and will be binding on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

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PATENT

REEL: 011580 FRAME: 0641

TO BE BINDING on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

FURTHER COVENANT that all statements made of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

(Signature) _	Jonathan F. Churchill	Date: 19 Feb 200
(Signature) _	Jeffrey F. Kooiman	Date:
(Signature) _	Cathal G. Phelan	Date:
(Signature) _	Ashish S. Pancholy	Date:
	Gary A. Gibbs	Date:

Please direct all communication as follows:

Kevin L. Daffer Conley, Rose & Tayon, P.C. P.O. Box 398 Austin, Texas 78767-0398 Ph: (512) 476-1400

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PATENT REEL: 011580 FRAME: 0642

## **ASSIGNMENT**

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned, does hereby:

SELL, ASSIGN AND TRANSFER to CYPRESS SEMICONDUCTOR CORPORATION (the "Assignee"), having a place of business at 3939 North First Street, San Jose, CA, 95134 the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith, and is entitled "DIRECT BIT LINE-BIT LINE DEFECT DETECTION TEST MODE FOR SRAM", such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States;

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COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee; and

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Page 1 of 2

PATENT REEL: 011580 FRAME: 0643 TO BE BINDING on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

FURTHER COVENANT that all statements made of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

(Signature) _		Date:
	Jonathan F. Churchill	
(Signature)	Jeffley B. Kooiman	Date: 2/15/2001
(Signature)	Cathal G. Phelan	Date:
(Signature)		Date:
	Ashish S. Pancholy	
(Signature)_		Date:
	Gary A. Gibbs	

Please direct all communication as follows:

Kevin L. Daffer Conley, Rose & Tayon, P.C. P.O. Box 398 Austin, Texas 78767-0398 Ph: (512) 476-1400

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(Signature) _	Jonathan F. Churchill	Date:
(Signature) _	Jeffrey F. Kooiman	Date:
(Signature) _	Cathal G. Phelan	Date: 2/12/07
(Signature) _	Ashish S. Pancholy	Date: 2/12/01
(Signature) _	Gary A Gibbs	Date: Ich 12, 2001

Please direct all communication as follows:

RECORDED: 02/23/2001

Kevin L. Daffer Conley, Rose & Tayon, P.C. P.O. Box 398 Austin, Texas 78767-0398 Ph: (512) 476-1400

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