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Masayuki ARAKAWA, Yasuhito SAITO and Naotake
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Execution Date: February 26, 2001

2. Name and address of receiving party(ies):

Name: KABUSHIKI KAISHA TOSHIBA

Address: 72 Horikawa-cho, Saiwai-ku, Kawasaki-shi
JAPANAdditional name(s) and address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: February 26, 2001

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

6. Total applications and patents involved: 1

7. Total fee (37 CFR 3.41): \$40.00

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In consideration of value received, I, having a post office address at c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura 1-chome, Minato-ku, Tokyo 105-8001, Japan and a residence as stated below next to my name, the sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are listed below) of an invention described in an application for United States patent entitled:

STACKED SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME

said application having been filed on _____
and assigned Serial No. _____;
sell and assign to
KABUSHIKI KAISHA TOSHIBA
a corporation of Japan, having a business address at
72 Horikawa-cho, Saiwai-ku, Kawasaki-shi, Japan

its successors, assigns or nominees, hereinafter referred to as "Assignee", my entire right, title and interest in and to said invention as disclosed, shown and described in said application for United States patent: (check one)

☒ executed concurrently herewith;

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Oblon, Spivak, McClelland, Maier & Neustadt, P.C., Fourth Floor, 1755
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00S1058

Inventor: (Signature)

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