

03-27-2001



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PATENT

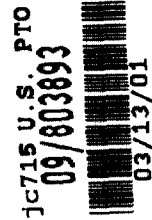
Docket No.: NAU015-1

3-13-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECORDING COVER SHEET

- 1. Assignor: Chin-Lai Chen , Tony Lin , Jih-Wen Chou
- 2. Assignee: UNITED MICROELECTRONICS CORP.
No.3, Li-Hsin Road 2,
Science-Based Industrial Park,
Hsin-Chu, Taiwan, R.O.C.
- 3. Nature of Conveyance: Assignment of patent
- 4. Reference number: Application filed herewith.
- 5. Correspondence to: Winston Hsu
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09/803893

- 6. Number of applications affected by this recording: 1
Total fee: (1 x \$ 40) = \$ 40.00 Payment by check enclosed
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- 7. Execution date of the document to be recorded: 03/05/2001
- 8. Total number of pages including cover sheet: 3
- 9. I declare under penalty of perjury that to the best of my knowledge and belief, the information contained on this cover sheet is true and correct, and that any copy submitted herewith is a true and correct copy of the original document.

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Date: 3/9/2001

Winston Hsu

Winston Hsu
U.S. Patent Agent
Reg. No.:41,526

ASSIGNMENT OF INVENTION

In consideration of the payment by ASSIGNEE to ASSIGNOR of the sum of One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for other good and valuable consideration,

ASSIGNORS (Inventors):

Name: Chin-Lai Chen Nationality: R.O.C

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Name: Jih-Wen Chou Nationality: R.O.C

Address: 6F, No.10, Aleey 156, Lane 376, Sec.1, Kuang-Fu Rd., Hsin-Chu City, Taiwan, R.O.C.

Name: _____ Nationality: _____

Address: _____

Hereby sells, assigns and transfers to UNITED MICROELECTRONICS CORP. (hereinafter "Assignee"), of (Assignee address) No.3, Li-Hsin Road 2, Science-Based Industrial Park, Hsin-Chu, Taiwan, R.O.C., and the successors assigns and legal representatives of the ASSIGNEE the entire right, title, and interest in and to any and all improvements which are disclosed in the invention entitled: " MOS transistor with two empty side slots on its gate and its method of formation. "

Which is found in :

- (a) _____ U.S. patent application executed on even date herewith
 (b) _____ U.S. patent application executed on _____
 (c) _____ U.S. application serial no. _____
 (d) _____ patent no. _____ issued _____