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FORM PTO-159  
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3/20/01



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Execution Date: March 9, 2001

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Name: KABUSHIKI KAISHA TOSHIBA

Address: 72 Horikawa-cho, Saiwai-ku  
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10971 U.S. PTO  
09/01/452  
03/20/01

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: **March 9, 2001**

A. Patent Application No.(s)

09/811452

B. Patent No.(s)

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22850

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In consideration of value received, I, having a mailing address at c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura 1-chome, Minato-ku, Tokyo 105-8001, Japan and a residence as stated below next to my name, the sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are listed below) of an invention described in an application for United States patent entitled:

## POWER SEMICONDUCTOR DEVICE HAVING A PROTECTION CIRCUIT

said application having been filed on \_\_\_\_\_  
and assigned Application No. \_\_\_\_\_ ;  
sell and assign to  
KABUSHIKI KAISHA TOSHIBA  
a corporation of Japan, having a business address at  
72 Horikawa-cho, Saiwai-ku, Kawasaki-shi, Japan

its successors, assigns or nominees, hereinafter referred to as "Assignee", my entire right, title and interest in and to said invention as disclosed, shown and described in said application for United States patent: (check one)

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Jefferson Davis Highway, Arlington, Virginia 22202

ASSIGNMENT

00S1400-1

Inventor: (Signature)

Date

Residence

*Tatsuo Yoneda*

**MAR. - 9. 2001**

Yokohama-shi, Japan

Tatsuo Yoneda

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