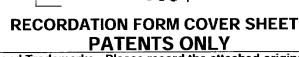
FORM PTO-1619A Expires 06/30/99 OMB 0651-0027 04-05-2001



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U.S. Department of Commerce Patent and Trademark Office

PATENT



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Name (line 1) Vladislav Vashchenko	3/15/2001
Name (line 2)	Execution Date
Name (line 1) Ann Concannon	3/15/2001
Name (line 2)	
Receiving Party	Mark if additional names of receiving parties attached
Name (line 1) National Semiconductor Corpor	is all assignment and the
Name (line 2)	receiving party is not domiciled in the United States, an appointment of a domestic
Ar dress (line 1) 2900 Semiconductor Drive	representative is attached. (Designation must be a separate document from
Address (line 2) M/S D3-579	Assignment.)
Address (line 3) Santa Clara	State/Country Zip Code
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**REEL: 011640 FRAME: 0536** 

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OMB 0651-	0027

## Page 2

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Correspondent Name and Address Area Code and Telephone Number 415-617-2100
Name Jurgen K. Vollrath
Address (line 1) Arter & Hadden, LLP
Address (line 2) Two Embarcadero Center, 5th Floor
Address (line 3) San Francisco, CA 94111
Address (line 4)
Pages Enter the total number of pages of the attached conveyance document including any attachments.
Application Number(s) or Patent Number(s) Mark if additional numbers attached
Enter either the Patent Application Number or the Patent Number (DO NOT ENTER BOTH numbers for the same property).
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If this document is being filed together with a <u>new</u> Patent Application, enter the date the patent application was signed by the first named executing inventor.
Patent Cooperation Treaty (PCT)
Enter PCT application number  PCT PCT PCT PCT
only if a U.S. Application Number PCT PCT PCT
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Number of Properties  Enter the total number of properties involved. # 1
Fee Amount Fee Amount for Properties Listed (37 CFR 3.41): \$ 40.00
Method of Payment: Enclosed 🗶 Deposit Account Deposit Account
(Enter for payment by deposit account or if additional fees can be charged to the account.)  Deposit Account Number: # 01-2520
Authorization to charge additional fees: Yes No
Statement and Signature
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Charges to deposit account are authorized, as indicated herein.  Integral K. Volulania
Jurgen K. Vollrath J. Wollack C. Wollack 3/19/01
Name of Person Signing Signature Date

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Conveying Pa	rty(ies)	Mark if additi	ional names of conveying	narties attached	- 10 P
Enter additional C	=	Wark ii addid	ional names of conveying	parties attached	Execution Date Month Day Year
Name (line 1) Pet	er J. Hopper				3/15/2001
Name (line 2)					Execution Date
Name (line 1)					Month Day Year
Name (line 2)					Execution Date
Name (line 1)					Month Day Year
Name (line 2)					
Receiving Par	rty(ies)		Mark if additional names	of receiving parti	es attached
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Name (line 1)					If document to be recorded is an assignment and the receiving party is not
Name (line 2)					domiciled in the United States, an appointment
Address (line 1)					of a domestic representative is attached. (Designation must be a separate
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Name (line 1)					If document to be recorded is an assignment and the
Name (line 2)					receiving party is not domiciled in the United States, an appointment of a
Address (line 1)					domestic representative is attached. (Designation must be a separate document from
Address (line 2)					Assignment)
Address (line 3)	City		State/Country	Zip C	Code
Application N	umber(s) or Patent N	lumber(s)	Mark if additional n		
	Patent Application Number or				ame property).
Pa	tent Application Number	(s)		Patent Numl	per(s)

PATENT

**REEL: 011640 FRAME: 0538** 

Docket Number (Optional)

PTO/SB/15 (08-00)
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ASSIGNMENT OF APPLICATION	72370/10174
Whereas, I, Vladislav Vashchenko of 94538	#2064, Fremont, CA , hereafter
referred to as applicant, have invented certain new and useful improvements HIGH HOLDING VOLTAGE ESD PROTECTION STRUCTURE AND METHOD	s in
for which an application for a United States Patent was filed on  Application Number/  X for which an application for a United States Patent was executed on	2/15/01
Whereas, National Semiconductor  2900 Semiconductor Corporation, M/S D3-5  "assignee" whose mailing address is 95051	•
2900 Semiconductor Corporation, M/S D3-5 "assignee" whose mailing address is 95051	579, Santa Clara, CA is de-
sirous of acquiring the entire right, title and interest in the same;	
Now, therefore, in consideration of the sum of ten dollars (\$\frac{10.00}{}}	), the receipt whereof is ac-
knowledged, and other good and valuable consideration, I, the applicant, by	these presents do sell, assign
and transfer unto said assignee the full and exclusive right to the said invention	on in the United States and the
entire right, title and interest in and to any and all Patents which may be grante	ed therefor in the United States,
I hereby authorize and request the Commissioner of Patents and Trademar	rks to issue said United States
Patent to said assignee, of the entire right, title, and interest in and to the same	ne, for his sole use and behoof;
and for the use and behoof of his legal representatives, to the full end of the t	erm for which said Patent may
be granted, as fully and entirely as the same would have been held by me ha	d this assignment and sale not
been made.	
at National Semiconductor Corpora	,20 c/
State of Cili forma SS:  County of Santa clara  Before me personally appeared said Viadislav Vish Chand acknowledged the foregoing instrument to be his free act and deed this day of March , 2001.	(Signature)
Seal	(Notary Public)

Burden Hour Statement: This form is estimated to take 0.1 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner of Patents and Trademarks, Washington, DC 20231.



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ASSIGNMENT OF A	PPLICATION	72370/10174
Whereas, I, Peter J. Hopper	4327 Verdigris Circle,	San Jose, CA 95134, hereafter
referred to as applicant, have invented cert HIGH HOLDING VOLTAGE ESD PROTECTION	tain new and useful improveme	
for which an application for a United Application Number//		
X for which an application for a United	d States Patent was executed	on $3/15/01$ , and
Whereas, National Semiconductor  2900 S	of	herein referred to
"assignee" whose mailing address is 95051	omoondator Corporation, 1420 B	is de-
sirous of acquiring the entire right, title and	l interest in the same;	
Now, therefore, in consideration of the sum	oft <u>en</u> dollars (\$_10.0	0), the receipt whereof is ac-
knowledged, and other good and valuable	consideration, I, the applicant,	by these presents do sell, assign
and transfer unto said assignee the full and	l exclusive right to the said inve	ention in the United States and the
entire right, title and interest in and to any ar	nd all Patents which may be gra	anted therefor in the United States,
I hereby authorize and request the Commi	issioner of Patents and Trader	marks to issue said United States
Patent to said assignee, of the entire right,	title, and interest in and to the s	same, for his sole use and behoof;
and for the use and behoof of his legal repr	esentatives, to the full end of th	ne term for which said Patent may
be granted, as fully and entirely as the sam	ie would have been held by me	had this assignment and sale not
been made.		
Executed this	day of MARCH	,20 <u>O 1</u> ,
at National Scinicand	Leter Corpora	tion
	$\Re \Lambda$	$ \swarrow $
State of State (June) SS:  County of State (June)  Before me personally appeared said and acknowledged the foregoing instrument day of March, 2001.	t to be his free act and deed this	(Signature)
Soal	ine	Materia Publica
Seal		(Notary Public)

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**PATENT REEL: 011640 FRAME: 0540** 

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**ASSIGNMENT OF APPLICATION** 

ASSIGNMENT OF APPLICA	TION	72370/10174	
Whereas, I, Ann Concannon o	2147 Coastland Avenue, S	San Jose, CA 95125	
referred to as applicant, have invented certain new a			
HIGH HOLDING VOLTAGE ESD PROTECTION STRUCT		III	
for which an application for a United States F Application Number/	atent was filed on		
Application Number/  for which an application for a United States F	Patent was executed on _	3/15/01, and	
Whereas, National Semiconductor of 2900 Semiconductor		herein referred to	
"assignee" whose mailing address is <u>95051</u>	ctor Corporation, M/S D3-57	'9, Santa Clara, CA is de-	
sirous of acquiring the entire right, title and interest i	n the same;		
Now, therefore, in consideration of the sum of <u>ten</u>	dollars (\$_10.00	_), the receipt whereof is ac-	
knowledged, and other good and valuable considera	ition, I, the applicant, by t	nese presents do sell, assign	
and transfer unto said assignee the full and exclusive	e right to the said inventio	n in the United States and the	
entire right, title and interest in and to any and all Pate	ents which may be granted	d therefor in the United States,	
I hereby authorize and request the Commissioner of	f Patents and Trademark	s to issue said United States	
Patent to said assignee, of the entire right, title, and i	nterest in and to the same	e, for his sole use and behoof;	
and for the use and behoof of his legal representative	es, to the full end of the te	rm for which said Patent may	
be granted, as fully and entirely as the same would h	ave been held by me had	this assignment and sale not	
been made.			
Executed this day of _	March	, 20_ <i>O</i>  ,	
at National Saniculus t	er Corporati	ga.	
	(h	new	
State of California ss:	(	Signature)	
County of Santa (lava)  Before me personally appeared said	Carcanion		
and acknowledged the foregoing instrument to be his free act and deed this 15th			
day of March, 20 01.	Kano	Theti	
Seal	(1)	lotary Public)	

Burden Hour Statement: This form is estimated to take 0.1 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner of Patents and Trademarks, Washington, DC 20231.



**RECORDED: 03/21/2001** 

**PATENT REEL: 011640 FRAME: 0541**