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1. Name of conveying party(ies): Siva G. Narendra Yibin Ye Vivek K. De Additional name(s) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		2. Name and address of receiving party(ies): INTEL CORPORATION 2200 Mission College Boulevard Santa Clara, CA 95052 Additional names/addresses attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other: Execution Date: 3/29/01		4. Application number(s) or patent number(s): If this document is being filed with a new application, the execution date of the application is: 3/29/01 A. Patent Application No(s).: B. Patent No(s).: 09823633 Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
5. Name/address of party to whom correspondence concerning document should be mailed: SCOTT C. HARRIS Fish & Richardson P.C. 4350 La Jolla Village Drive, Suite 500 San Diego, CA 92122		6. Total number of applications/patents involved: 1 7. Total fee (37 CFR §3.41): \$40 <input checked="" type="checkbox"/> Enclosed <input type="checkbox"/> Authorized to charge Deposit Account. 8. Deposit Account No.: 06-1050 Please apply any additional charges, or any credits, to our Deposit Account No. 06-1050.	
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March 30, 2001
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PATENT
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ASSIGNMENT

For valuable consideration, we, SIVA G. NARENDRA of Beaverton, Oregon, YIBIN YE of Hillsboro, Oregon, and VIVEK K. DE of Beaverton, Oregon, hereby assign to: Intel Corporation, a Delaware corporation having a place of business at: 2200 Mission College Boulevard, Santa Clara, CA 95052; and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us, entitled TRADING OFF GATE DELAY VERSUS LEAKAGE CURRENT USING DEVICE STACK EFFECT, filed _____, and assigned U.S. Serial Number _____, and we authorize and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the filing date and serial number of said application when known; this assignment including said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and we authorize the Assignee to apply in all countries in our name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and we agree for ourselves and our respective heirs, legal representatives and assigns, without further compensation to perform such lawful acts and to sign such further applications, assignments, Preliminary Statements and other lawful documents as the Assignee may reasonably request to effectuate fully this assignment.

Date: 3/29/01Date: 3/29/01Date: 3/29/01

 SIVA G. NARENDRA


 YIBIN YE


 VIVEK K. DE

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