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05-10-2001

U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office
Docket 00-307

| | Docket 00-507 | | | | | |
|---|--|--|--|--|--|--|
| To the Commissioner for Patents : Please record and | 01711943 _py thereof. | | | | | |
| 1. Name of conveying party(ies): | 2. Name and address of receiving party(ies): | | | | | |
| Azeez J. Bhavnagarwala | | | | | | |
| Additional name(s) of conveying party(ies) attached? [] Yes [x] No 3. Nature of conveyance: | LSI Logic Corporation 1551 McCarthy Boulevard Milpitas, CA 95035 | | | | | |
| [x] Assignment [] Merger [] Security Agreement [] Change of Name [] Other | | | | | | |
| Execution Date: April 20, 2001 | | | | | | |
| 4. Application number(s) or patent number(s): 09/737,8 | 337 | | | | | |
| If this document is being filed together with a new application, the execution date of the application is: January 19, 2001 | | | | | | |
| | | | | | | |
| A. Patent Application No.(s) 09/737,837 | B. Patent No.(s) | | | | | |
| Additional numbers attached? [] Yes [x] No | | | | | | |
| 5. Name and address of party to whom correspondence concerning document should be mailed: | 6. Total number of applications and patents involved: 1 | | | | | |
| LSI LOGIC CORPORATION | 7. Total fee (37 CFR 3.41): \$ 40.00 | | | | | |
| 1551 McCarthy Blvd. Milpitas, CA 95035 (408) 433-8708 | [] Enclosed [x] Authorized to be charged to deposit account should the fee not be enclosed. | | | | | |
| | 8. Deposit account number: 12-2252 | | | | | |
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| 9. Statement and signature. | | | | | | |
| To the best of my knowledge and belief, the foregoing inf copy of the original document. | formation is true and correct and any attached copy is a true | | | | | |
| 6 1 Olt 11 | | | | | | |
| Eric I. Whitesell | April 24, 2001 Date | | | | | |
| Registration No. 38,657 | | | | | | |
| 10. Total number of pages including cover sheet, attachments and document: 2 | | | | | | |

PATENT REEL: 011773 FRAME: 0935 Docket No.: 00-307 Patent

68577 (6653)

ASSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/WE, the undersigned, Azeez J. Bhavnagarwala and Ashok Kapoor , do hereby sell, assign, and transfer to: LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at 1551 McCarthy Boulevard, Milpitas, California 95035, ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements which are disclosed in the application for United States Letters Patent.

() which has been executed by the undersigned concurrently herewith.

(x) which was filed on <u>December 15, 2000</u> and assigned Serial Number 09/737,837.

and is entitled: SOURCE PULSED, DYNAMIC THRESHOLD COMPLEMENTARY METAL OXIDE SEMICONDUCTOR STATIC RAM CELLS and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and

Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assignee, its successors, assigns, and representatives, all facts known to the undersigned relating to said improvements and the history thereof; and generally do everything possible which said Assignee, its successors, assigns or representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications to patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives; and

Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

| Name: | \sim | \cup | 0 | \checkmark | \sim |
|-------|----------|---------------|---|--------------|--------|
| , | Azeez J. | Bhavnagarwala | | | |

5 Third Street, Apt. A1 Danbury, CT 06810

Date: 4/20/01

Witness

Witness Printed

Name:

Date:

Witness:

Witness: Printed

Ashok Kapoor 1056 Amarillo Avenue Palo Alto, CA 94303

PATENT REEL: 011773 FRAME: 0936