08-10-2001 FORM PTO-1596 1-31-92 Docket No.: 49657-999 101805536 To the Honorable Commissioner for ched original documents or copy thereto: 1. Name of Conveying Party(ies): .dress of receiving party(ies): Yutaka IKEDA Name: Mitsubishi Denki Kabushiki Kaisha Address: 2-3, Marunouchi 2-chome, Chiyoda-ku Additional name(s) of conveying party(ies) attached? Yes 3. Nature of Conveyance: TOKYO 100-8310, JAPAN **Assignment** Merger Security Agreement ☐ Change of Name Other Execution Date: January 11, 2001 Additional name(s) & address(es) attached? Yes 4. Application number(s) or patent number(s): If the document is being filed together with a new application, the execution date of the application is: January 11, 2001 A. Patent Application No(s). B. Patent No(s). Additional numbers attached? Yes 5. Name and address of party to whom correspondence 6. Total number of applications and patents involved: concerning document should be mailed: Name: MCDERMOTT, WILL & EMERY 7. Total fee (37 CFR 3.41) \$40.00 Enclosed Internal Address: Authorized to be charged to deposit account 600 13th Street, N.W. Street Address: 8. Deposit account number: 500417 40E City: Washington DC Zip: 20005 State: DO NOT USE THIS SPACE 9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. February 8, 2001 Stephen A. Becker, 26,527 Date Name and Registration No. of Person Signing Signature Total number of pages comprising cover sheet: CMB No. 0851-0011 (exp. 4/94)

> **PATENT** REEL: 011847 FRAME: 0963

Docket No.:

RECORDED: 02/08/2001

ASSIGNMENT

is hereby acknowledged, the undersi	-	(7)
(1) Yutaka IKEDA (2)	(4)	(7)
(2)	(5)	(8)
(3)		
who have made a certain new and u	seful invention, hereby sell, assign and tr	ansfer unto
<u>Mitsubishi</u> Denki Kab	oushiki Kaisha of	
	nome, Chiyoda-ku, TOKYO	100-8310 JAPAN
its successors and assigns (hereinaft as defined in 35 U.S.C. 100 in the i		ght, title and interest for the United States of Americ
SEMICONDUCTOR DEVICE	WITH REDUCED ERROR OPER	RATION CAUSED BY THRESHOLD
VOLTAGE VARIATION		
(a) for which an application for Un Serial No.	nited States Letters Patent was filed on; or	, and identified by United State
(b) for which an application for Ur	nited States Letters Patent was executed or	January 11, 2001 .
United States Letters Patent which continuations, or continuations-in-pale Industrial Property to the said ASS undersigned agree that the attorneys AND the undersigned hereband obtaining original, divisional, resuccessors, assigns and legal representations of the said continuous and the said and t	may be granted therefor and any and all art thereof, and the right to all benefits un SIGNEE, for its interest as ASSIGNEE, so frecord in said application shall hereaf by agree to transfer a like interest, and to recissued or extended Letters Patent of the Untatives, and without further remuneration, ated to the said invention; and to execute an	sioner of Patents and Trademarks to issue any and all extensions, divisions, reissues, substitutes, renewals der the International Convention for the Protection of its successors, assigns and legal representatives; there act on behalf of said ASSIGNEE: ender all necessary assistance in making application for Juited States, upon request of the said ASSIGNEE, it in and to any improvements, and applications for patenty papers by the said ASSIGNEE, its successors, assigned title in and to the invention hereby transferred.
SIGNED on the dates indicated asid INVENTORS,	_	DATE SIGNED
1) Yutuka Uk Name: Yutaka IKEDA	keda	January 11, 2001
Name: Yutaka IKEDA		
2)		
2) Name:		
Name:		
4) Name:		
5) Name:		
6) <u>Name:</u>		
7) Name:		
8)		
8) Name:		
9)		

PATENT REEL: 011847 FRAME: 0964