

06/16/01

JP

ASSIGNMENT RECORDATION COVER SHEET  
-PATENTS ONLY-



To: Honorable Commissioner of Patents and Trademarks:

Please record the attached original document(s) or copy thereof.

1. Name of conveying party(ies)

- a) Mehmit A. Cirit  
b)  
c)

06/08/01

2. Name and address of receiving party(ies):

- a) Name: Library Technologies, Inc.  
Address: 19959 Lanark Lane  
Saratoga, California 95070

06-19-2001



101751122

3. Nature of conveyance

- |  |  |
|--|--|
| <input checked="" type="checkbox"/> Assignment | <input type="checkbox"/> Merger            |
| <input type="checkbox"/> Security Agreement    | <input type="checkbox"/> Change of Name    |
| <input type="checkbox"/> Other _____           | <input type="checkbox"/> License Agreement |

Execution Date: Mehmit A. Cirit, June 7, 2001

4. Application Number(s) or Patent Number(s): Please assign

The title of the (new) application is:

09/877998

**Apparatus and Methods for Wire Load Independent Logic Synthesis and Timing Closure with Constant Replacement Delay Cell Libraries**

5. Please send all correspondence concerning this (these) documents to:

**Customer Number: 022434**  
**BEYER WEAVER & THOMAS, LLP**  
P.O. Box 778  
Berkeley, CA 94704-0778  
Phone (510) 843-6200  
Fax (510) 843-6203

6. Total number of applications and patents involved: 1

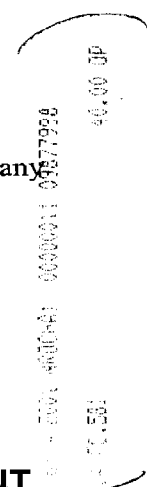
7. Total fee (37 CFR 3.41): \$40.00

- ☒ Enclosed  
☒ Any additional fees are authorized to be charged to Deposit Account No. 500388 (Order No. LTECP001)

8. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date: June 8, 2001

Mary R. Olynick  
Registration No. 42,963



Attorney Docket No. LTECP001

(Revised 01/96)

**PATENT**  
**REEL: 011892 FRAME: 0183**

# ASSIGNMENT OF PATENT APPLICATION

(Accompanying Application)

Whereas I/we the undersigned inventor(s) have invented certain new and useful improvements as set forth in the patent application entitled:

## **APPARATUS AND METHODS FOR WIRE LOAD INDEPENDENT LOGIC SYNTHESIS AND TIMING CLOSURE WITH CONSTANT REPLACEMENT DELAY CELL LIBRARIES**

(Atty. Docket No.: LTECP001) for which I (we) have executed an application for a United States Letters Patent.

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/We the undersigned inventor(s) hereby:

- 1) Sell(s), assign(s) and transfer(s) to Library Technologies, Inc., a California corporation having a place of business at 19959 Lanark Lane, Saratoga, CA 95070, (hereinafter referred to as "ASSIGNEE"), the entire right title and interest in any and all improvements and inventions disclosed in, application(s) based upon, and Patent(s) (including foreign patents) granted upon the information which is disclosed in the above referenced application.
- 2) Authorize and request the Commissioner of Patents to issue any and all Letters Patents resulting from said application or any division(s), continuation(s), substitutes(s) or reissue(s) thereof to the ASSIGNEE.
- 3) Agree to execute all papers and documents and, entirely at the ASSIGNEE's expense, perform any acts which are reasonably necessary in connection with the prosecution of said application, as well as any derivative and applications thereof, foreign applications based thereon, and/or the enforcement of patents resulting from such applications.
- 4) Agree that the terms, covenants and conditions of this assignment shall inure to the benefit of the Assignee, its successors, assigns and other legal representative, and shall be binding upon the inventor(s), as well as the inventor's heirs, legal representatives and assigns.
- 5) Warrant and represent that I/we have not entered, and will not enter into any assignment, contract, or understanding that conflicts with this assignment.

Signed on the date(s) indicated beside my (our) signature(s).

1) Signature: Mehmet A. Cirit Date: 06/07/01  
Typed Name: Mehmet A. Cirit