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 (4) Takashi YONEDA (5) Nawa TAKANORI (6) Koji TSUNETO
 (7) Masuo INUI (8) Hiroyuki YAMAMOTO
2. Name and Address of receiving party(ies):
 FUJITSU LIMITED, 1-1, Kamikodanaka
 4-chome, Nakahara-ku, Kawasaki-shi
 Kanagawa 211-8588 Japan
- 06-20-2001

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3. Nature of conveyance:
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 Execution Date(s): March 21, 2001
4. Application number(s) or patent number(s):
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 (a) The execution date(s) of the application is/are:
 (b) The title is:
 *** OR ***
☒ This document is being filed after filing of the application:
 (a) Patent Application No(s). 09/812,552, filed March 21, 2001; or
 (b) Patent No(s). , issued .
5. Name and address of party to whom correspondence concerning document should be mailed:
 STAAS & HALSEY LLP Our Docket: 1448.1013
 Attention: H. J. Staas
 700 Eleventh Street, N.W., Suite 500
 Washington, D.C. 20001
6. Total number of applications and patents involved: 1
7. Total fee (37 CFR 3.41)..... (\$ 40.00 per Assignment)
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1. Name of conveying party(ies):

- (1) Kenji SUZUKI
(2) Koji BANNO
(3) Toru OSAJIMA
(4) Takashi YONEDA

- (5) Takanori NAWA
(6) Koji TSUNETO
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2. Name and address of receiving party(ies):

FUJITSU LIMITED
1-1, Kamikodanaka 4-chome
Nakahara-ku, Kawasaki-shi
Kanagawa 211-8588, JAPAN

3. Nature of conveyance:

- ☒ Assignment ☐ Merger
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Execution Date(s): March 15, 2001

09/812552

4. Application number(s) or patent number(s):

This document is being filed together with a new application:

- (a) The execution date(s) of the application is/are: March 15, 2001
(b) The title is: METHOD OF DESIGNING WIRING FOR POWER SOURCES IN A SEMICONDUCTOR CHIP, AND A COMPUTER PRODUCT

*** OR ***

This document is being filed after filing of the application:

- (a) Patent Application No(s). _____, filed _____; or
(b) Patent No(s). _____, issued _____.

5. Name and address of party to whom correspondence concerning document should be mailed:

STAAS & HALSEY LLP
Attention: H. J. Staas
700 Eleventh Street, N.W., Suite 500
Washington, D.C. 20001

Our Docket: 1448.1013/HJS

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41)..... \$ 40.00

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j1017 U.S. PTO
09/812552
03/21/01

U.S. ASSIGNMENT

6/8 7/94

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter, "ASSIGNOR") by

(Insert Name(s) & Address(es) of ASSIGNEE(S))

FUJITSU LIMITED

1-1, Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi,

Kanagawa 211-8588 Japan

(hereinafter, "ASSIGNEE"), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

(Title of Invention)

METHOD OF DESIGNING WIRING FOR POWER SOURCES IN

A SEMICONDUCTOR CHIP, AND A COMPUTER PRODUCT

relating to International Patent Application PCT/JP____/____ and/or for which application for Letters Patent of the United States was executed on even date herewith or, if not so executed, was:

(a) executed on March 15, 2001; (Insert date of execution of application, if not concurrent)

(b) filed on _____,

Serial No. ____/____;

Any registered attorney of STAAS & HALSEY, 700 Eleventh Street, N.W., Washington, D.C. 20001 (202/434-1500) is hereby authorized to insert in (b) the specified date, when known.

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IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

(Typed Name & Signature of Inventor(s))

(Date)

(Typed Name & Signature of Witness(es))

1) <u>鈴木 賢司</u> Kenji SUZUKI	<u>March 15, 2001</u>	<u>Yasuko Watanabe</u> Yasuko WATANABE
2) <u>板野 幸司</u> Koji BANNO	<u>March 15, 2001</u>	<u>Yasuko Watanabe</u> Yasuko WATANABE
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4) <u>Takashi Yoneda</u> Takashi YONEDA	<u>March 15, 2001</u>	<u>Yasuko Watanabe</u> Yasuko WATANABE
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PATENT

REEL: 011896 FRAME: 0223

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8) 山本 弘幸
Hiroyuki YAMAMOTO

March 15, 2001

Yasuko Watanabe
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