

07-06-2001



101769016

## PATENTS ONLY

U.S. DEPARTMENT OF COMMERCE  
U.S. Patent and Trademark Office

Form PTO-1595

(Rev. 03/01)

OMB No. 0651-0027 (exp. 5/31/2002)

Tab settings

RECOR

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

## 1. Name of conveying party(ies):

Alesis Semiconductor Corporation  
a/k/a AlesisAdditional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

## 3. Nature of conveyance:



Assignment



Merger



Security Agreement



Change of Name



Other

Execution Date: June 15, 2001

## 2. Name and address of receiving party(ies)

Name: Alesis Distribution, Inc.

Internal Address: \_\_\_\_\_

Street Address: c/o Duffy &amp; Sweeney, LTD

300 Turks Head Building

City: Providence State: RI Zip: 02903

Additional name(s) & address(es) attached? ☐ Yes ☒ No

## 4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: \_\_\_\_\_

A. Patent Application No.(s)

B. Patent No.(s)

6,163,197

Additional numbers attached? ☐ Yes ☒ No

## 5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Craig M. Scott, Esq.

Internal Address: \_\_\_\_\_

Duffy & Sweeney, LTD  
Street Address: 300 Turks Head Building

City: Providence State: RI Zip: 02903

6. Total number of applications and patents involved: ☒ 1

7. Total fee (37 CFR 3.41).....\$ 40.00

☒ Enclosed☐ Authorized to be charged to deposit account

## 8. Deposit account number:

(Attach duplicate copy of this page if paying by deposit account)

DO NOT USE THIS SPACE

## 9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Craig M. Scott, Esq.

Name of Person Signing

Signature

June 27, 2001  
Date

07/05/2001 00000229 6163197 Total number of pages including cover sheet, attachments, and documents: 2

01 FC:581

40.00

Mail documents to be recorded with required cover sheet information to:  
Commissioner of Patents & Trademarks, Box Assignments  
Washington, D.C. 20231PATENT  
REEL: 011934 FRAME: 0593

# ASSIGNMENT OF PATENT

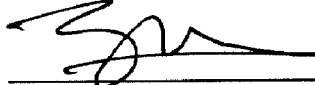
WHEREAS, Alesis Semiconductor Corporation a/k/a Alesis Semiconductor, Inc., hereinafter referred to as "ASSIGNOR," is the current Assignee of a United States Patent for an improvement in "Low Noise Method for Interconnecting Analog and Digital Integrated Circuits," No. 6,163,197, dated December 19, 2000, and whereas, it is now the sole owner of said patent, and,

WHEREAS, Alesis Distribution, Inc., a Delaware Corporation, hereinafter referred to as "ASSIGNEE" whose mailing address is c/o Duffy & Sweeney, LTD, 300 Turks Head Building, Providence, RI 02903, is desirous of acquiring the entire right, title and interest in the same;

NOW THEREFORE, in consideration of the sum of ten dollars (\$10.00), and other good and valuable consideration, the receipt whereof is acknowledged, the ASSIGNOR, by these presents does sell, assign and transfer unto said ASSIGNEE the entire right, title and interest in and to the said Patent aforesaid; the same to be held and enjoyed by the said ASSIGNEE for its own use and behoof, including, but not limited to, the right to sue for infringement that occurred prior to the date hereof; and for the use and behoof of its legal representatives and assigns, to the full end of the term for which said Patent is granted, as fully and entirely as the same would have been held by the ASSIGNOR had this assignment and sale not been made.

Executed this 15 day of June, 2001, at LOS ANGELES.

Alesis Semiconductor Corporation a/k/a  
Alesis Semiconductor, Inc.



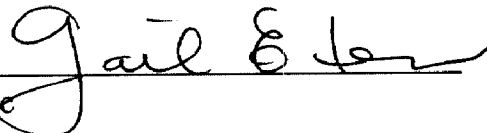
By: KEITH BARR  
Title: CEO

State of California

County of LOS ANGELES

Before me personally appeared said SS: KEITH BARR and  
acknowledged the foregoing instrument to be his/her free act and deed this 15th day of  
June, 2001.

Notary Public



odonnell\alesis\documents\Assignment of Patent - Alesis 3

