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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

**MKD 03-21-01**

1. Name of conveying party(ies):

(1) Yuzi KANAZAWA

2. Name and address of receiving party(ies):

FUJITSU LIMITED  
1-1, Kamikodanaka 4-chome, Nakahara-ku  
Kawasaki, 211-8588 JAPAN

04-04-2001



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3. Nature of conveyance:

Assignment  Merger  
 Security Agreement  Change of Name  
 Other: \_\_\_\_\_

Execution Date(s): March 5, 2001

4. Application number(s) or patent number(s):

This document is being filed together with a new application:

(a) The execution date(s) of the application is/are: March 5, 2001

(b) The title is: LAYOUT INSTRUMENT FOR SEMICONDUCTOR INTEGRATED CIRCUITS, LAYOUT METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUITS AND RECORDING MEDIUM THAT STORES A PROGRAM FOR DETERMINING LAYOUT OF SEMICONDUCTOR INTEGRATED CIRCUITS

\*\*\* OR \*\*\*

This document is being filed after filing of the application:

(a) Patent Application No(s). \_\_\_\_\_, filed \_\_\_\_\_; or

(b) Patent No(s). \_\_\_\_\_, issued \_\_\_\_\_.

**09812848**

5. Name and address of party to whom correspondence concerning document should be mailed:

STAAS & HALSEY LLP  
Attention: James D. Halsey, Jr.  
700 Eleventh Street, N.W., Suite 500  
Washington, D.C. 20001

Our Docket: 826.1708/JDH

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41)..... \$ 40.00

Enclosed  
 Authorized to be charged to deposit account.

8. Deposit Account No.: 19-3935 (Any underpayment is authorized to be charged to this Deposit Account)  
(Attach duplicate copy of this page if paying by deposit account)

9. Statement and signature.

*To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.*

James D. Halsey, Jr., Reg. No. 22,729

Name of Person Signing

Signature

March 20, 2001

Date

Total number of pages including cover sheet: 2

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# U.S. ASSIGNMENT

5&H 7/84

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter, "ASSIGNOR") by

(Insert Name(s) & Address(es) of ASSIGNEE(S))

FUJITSU LIMITED

1-1, Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi, Kanagawa

211-8588, Japan

(hereinafter, "ASSIGNEE"), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

(Title of Invention) LAYOUT INSTRUMENT FOR SEMICONDUCTOR INTEGRATED CIRCUITS,  
LAYOUT METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUITS AND RECORDING MEDIUM  
THAT STORES A PROGRAM FOR DETERMINING LAYOUT OF SEMICONDUCTOR INTEGRATED  
CIRCUITS

relating to International Patent Application PCT/JP \_\_\_\_\_/\_\_\_\_\_ and/or for which application for Letters Patent of the United States was executed on even date herewith or, if not so executed, was:

(a) executed on \_\_\_\_\_; (Insert date of execution of application, if not concurrent)

(b) filed on \_\_\_\_\_,  
Serial No. \_\_\_\_\_/\_\_\_\_\_;

Any registered attorney of STAAS & HALSEY, 700 Eleventh Street, N.W., Washington, D.C. 20001 (202/434-1500) is hereby authorized to insert in (b) the specified date, when known.

and to said application and all Letters Patent(s) of the United States granted on said application and any continuation, division, renewal, substitute, reissue or reexamination application based thereon, for the full term or terms for which the said Letters Patent(s) may be granted and including any extensions thereof (collectively, hereinafter, "said application(s) and Letters Patent(s)").

The ASSIGNOR agree(s), when requested by said ASSIGNEE and without charge to but at the expense of said ASSIGNEE, to do all acts which the ASSIGNEE may deem necessary, desirable or expedient, for securing, maintaining and enforcing protection for said invention, including in the preparation and prosecution of said application(s) and the issuance of said Letters Patent(s), in any interference, reissue, reexamination, or public use proceeding, and in any litigation or other legal proceeding which may arise or be declared in relation to same, such acts to include but not be limited to executing all papers, including separate assignments and declarations, taking all rightful oaths, providing sworn testimony, and obtaining and producing evidence.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

(Typed Name & Signature of Inventor(s))

(Date)

(Typed Name & Signature of Witness(es))

1)	Yuzi KANAZAWA	March 5, 2001	Hiroimi IJITSU
2)			
3)			