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1 1	Minato-Ku, Tokyo, JAPAN	09/		
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Execution Date: August 8, 2001	2212011			
Application Number(s) or Patent Number(s):	73102Y			
If this document is being filed together with a new application, the	e execution date of the application is: August 8, 2001			
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## ASSIGNMENT

In consideration of value received, I, having a mailing address at cool intellectual Property Division, KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura 1 chome, Minato-ku, Tokyo 105-8001, Japan and a residence as stated below next to my name, the sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are listed below) of an invention described in an application for United States patent entitled:

FUSE CIRCUIT USING ANTI-FUSE AND METHOD FOR SEARCHING FOR FAILED ADDRESS IN SEMICONDUCTOR MEMORY

said application having been filed on and assigned Application No. :
sell and assign to
KABUSHIKI KAISHA TOSHIBA
a corporation of Japan, having a business address at 1-1 Shibaura 1-chome, Minato-Ku, Tokyo, Japan

its successors, assigns or nominees, hereinafter referred to as "Assignee", my entire right, title and interest in and to said invention as disclosed, shown and described in said application for United States patent: (check one)

x executed concurrently herewith:
 executed separately herefrom;

and in and to all applications for patent and patents for said invention, in all countries of the world, including all divisions, reissues, continuations, substitutions and extensions thereof and all rights arising under or pursuant to any and all international agreements, treaties or laws relating to the protection of industrial property, including rights of priority, resulting from the filing of any of said applications; and I authorize and request any official whose duty is to issue patents. to issue any patent on said invention or resulting therefrom to said Assignee, and I agree that on request and without further consideration. but at the expense of said Assignee, I will communicate to said Assignee or its representatives all facts known to me respecting said invention and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuing, reissue, or other applications, make all rightful oaths and declarations, and generally do everything possible to aid said Assignce to obtain and enforce proper patent protection for said invension in all countries.

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Inventor: (Signature)	Date	Residence
Hamiki Toda	ATTO 0 2001	
Maraki Loda	AUG 0. <b>2001</b>	Yokohama-shi, Japan
Haruki Toda		

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