

08-30-2001

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101828407

To the Honorable Commissioner of Patents and Trademarks, Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

ROCKWELL SCIENCE CENTER, LLC

8-2301

Additional names(s) of conveying party(ies) ☐ Yes ☒ No

3. Nature of conveyance:

☒ Assignment☐ Merger☐ Security Agreement☐ Change of Name☐ Other

Execution Date: December 10, 1998

2. Name and address of receiving party(ies):

Name: CONEXANT SYSTEMS, INC.

Internal Address:

Street Address: 4311 Jamboree Road

City: Newport Beach State: CA ZIP: 92660

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or registration numbers(s):

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s)

B. Patent No.(s)

5,488,378

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Stephen Warhola

Internal Address: Conexant Systems, Inc.

Department 927, E08 803

Street Address: 4311 Jamboree Road

City: Newport Beach State: CA ZIP: 92660

6. Total number of applications and patents involved:

1

7. Total fee (37 CFR 3.41):.....\$ 40.00

☐ Enclosed - Any excess or insufficiency should be credited or debited to deposit account☒ Authorized to be charged to deposit account

8. Deposit account number:

500835

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Stephen Warhola Reg. No. 43,237

Name of Person Signing

Signature

July 29, 2001

Date

Total number of pages including cover sheet, attachments, and document:

14

PATENT

REEL: 012110 FRAME: 0108

10/29/2001 BY: 00000167 500835 5488378

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ASSIGNMENT OF INVENTIONS AND PATENTS

WHEREAS, Rockwell Science Center, LLC ("Assignor"), a limited liability company organized and existing under the laws of the State of Delaware, desires to assign to Conexant Systems, Inc., ("Assignee"), a corporation organized and existing under the laws of the State of Delaware, certain inventions and patents of Assignor; and

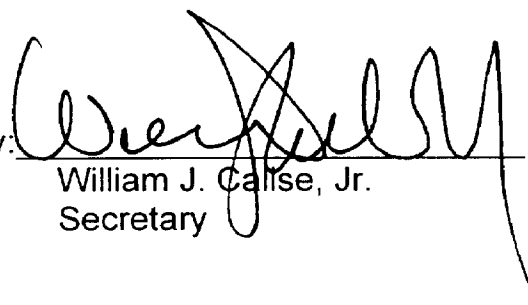
WHEREAS, Assignor is the owner of or has rights in, those inventions and patents which are used primarily in or relate to the semiconductor business conducted by Assignee, including those inventions and patents listed on the Schedule attached hereto ("Contributed Inventions and Patents").

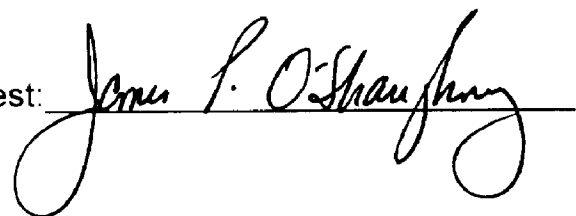
NOW THEREFORE, in consideration of good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged by Assignor, said Assignor does hereby sell, grant, assign, convey and transfer unto the said Assignee, Assignor's entire right, title and interest in and to the Contributed Inventions and Patents, including any continuations or divisions thereof, any and all re-examinations or reissues thereof and the subject matter of any and all claims which may be obtained in every such patent, as well as all corresponding foreign patents and patent applications in whatever jurisdictions in which the same exist along with all rights of priority and the right to file applications for patents based thereon and to prosecute such applications and receive patents thereon in its own name, all of the same to be held and enjoyed by the said Assignee, its successors, assigns or other legal representatives, for its own use and advantage, and for the use and advantage of its successors, assigns or other legal representatives, to the end of the term or terms for which patents are or may be granted, re-issued or re-examined, as fully and entirely as the same would have been held and enjoyed by Assignor had this assignment not been made, including all rights to profits and damage by reason of past infringement by any party or parties, with the right to sue and collect the same for its own use and benefit, and for the use and benefit of its successors, assigns or other legal representatives.

Assignor hereby appoints Assignee its attorney-in-fact to act in Assignor's name, place and stead to execute, deliver and record any document or instrument of assignment required in any country in which any of the Contributed Inventions and Patents are pending or issued, granting or confirming the rights granted herein, but only to the extent of those rights granted herein in connection with the Contributed Inventions and Patents.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be duly executed on this 10th day of December 1998.

Rockwell Science Center, LLC

By: 
William J. Calise, Jr.
Secretary

Attest: 

(Company Seal)

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
4,313,174	01/26/82	ROM-Based Parallel Digital Arithmetic Device
4,314,858		Method Of Making A Fully Integrated Monolithic Optical Receiver
4,315,319	02/09/82	Non-Linear Signal Processor,
4,315,477	02/16/82	Semi-Open Liquid Phase Epitaxial Growth System
4,321,584		Charge Coupled Digital-To-Analog Converter
4,326,936		Repeatable Method For Sloping Walls Of Thin Film Material
4,328,588	05/04/82	Synchronization System For Digital Data
4,335,504		Method Of Making CMOS Devices
4,337,132		Ion Etching Process With Minimized Redeposition
4,339,134	07/13/82	Electronic Card Game Macheel
4,339,305		Planar Circuit Fabrication By Plating And Liftoff
4,344,151	08/10/82	ROM-Based Complex Multiplier Useful For FFT Butterfly Arithmetic
4,344,675		Optical Signal Processing Device
4,347,476	08/31/82	Voltage-Temperature Insensitive On-Chip Reference Voltage Source Compatible With VLSI Manufacturing Techniques
4,350,916	09/21/82	Surface Acoustic Wave Device Having Buried Transducer
4,355,247		Sense Amplifier And Method For Small Bit Line Swing With Short Propagation Delay For High Speed MOS Memories
4,355,869		Self Scanned Optical Fourier Transform Arrangement
4,355,910		Method And Apparatus For An Optical Sensor Utilizing Semiconductor Filters
4,357,364	11/02/82	High Rate Resist Polymerization Method
4,361,814		Distributed Optical Parametric Amplifier
4,363,978		Reduced Power Tristate Driver Circuit
4,368,085	01/11/83	SOS Island Edge Passivation Structure
4,368,689		Beam Source For Deposition Of Thin Film Alloys
4,369,406	01/18/83	Semiconductor Magneto-Transistor Device
4,378,410	03/29/83	Compliant Glass For Solid State Devices
4,379,307		Integrated Circuit Chip Transmission Line
4,389,429	06/21/83	Method Of Forming Integrated Circuit Chip Transmission Line
4,389,538	06/21/83	Multiplexer For Single-Octave Data Processor
4,390,833		Voltage Regulator Circuit
4,394,182	07/19/83	Microelectronic Shadow Masking Process For Reducing Punchthrough
4,396,479		Ion Etching Process With Minimized Redeposition
4,399,373		FET Power Supply For MOS Memories
4,400,407	08/23/83	Method For Deposition Of Thin Film Alloys Utilizing Electron Beam Vaporization
4,401,694		Method And Apparatus For An Optical Sensor Utilizing Semiconductor Filters
4,404,265		Epitaxial Composite And Method Of Making
4,404,581		ROM With Redundant ROM Cells Employing A Highly Resistive Polysilicon Film For Programming The Cells
4,405,870		Schottky Diode-Diode Field Effect Transistor Logic
4,406,049	09/27/83	Very High Density Cells Comprising A ROM And Method Of Manufacturing Same
4,406,252	09/27/83	Inductive Heating Arrangement For Evaporating Thin Film Alloy Onto A Substrate
4,410,954		Digital Frequency Synthesizer With Random Jittering F/Reducing Discrete Spectral Spurs

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
4,414,507		Copper Embedded Ferrite Coil Arrangement F/Supplying Uniform Rotating Fld I/Full Frequency
4,415,371		Method Of Making Sub-Micron Dimensioned NPN Lateral Transistor
4,416,050		Method Of Fabrication Of Dielectrically Isolated CMOS Devices
4,419,150		Method Of Forming Lateral Bipolar Transistors
4,419,808	12/13/83	[Method Of Producing Redundant ROM Cells] ROM With Redundant ROM Cells Employing A Highly Resistive Polysilicon Film For Programming The Cells
4,430,587		MOS Fixed Delay Circuit
4,432,134	02/21/84	Process For In-Situ Formation Of Niobium-Insulator-Niobium Josephson Tunnel Junction Devices
4,435,899	03/13/84	Method Of Producing Lateral Transistor Separated From Substrate By Intersecting Slots Filled With Substrate Oxide
4,435,899		Producing Lateral Transistor Separated F/Substrate B/Intersecting Slots Filled S/Substrate
4,437,226		Process For Producing NPN Type Lateral Transistor With Minimal Substrate Operation Interference
4,444,617	04/24/84	Reactive Ion Etching Of Molybdenum Silicide And N + Polysilicon
4,445,183	04/24/84	Electrical Switch
4,447,497	05/08/84	CVD Process For Producing Monocrystalline Silicon-On-Cubic Zirconia And Article Produced Thereby
4,450,368	05/22/84	AC Coupled Chopper Stabilized Differential Comparator
4,454,481		Shielded Amplifier
4,455,267		Fabrication Of Birefringent Electromagnetic Transmission Line
4,455,737		Process For And Structure Of High Density VLSI Circuits, Having Self-Aligned Gates And Contacts For FET Devices And Conducting Lines
4,459,937	07/17/84	High Rate Resist Polymerization Apparatus
4,464,586		Squaring Circuit Bypass
4,466,178		Method Of Making Extremely Small Area PNP Lateral Transistor By Angled Implant Of Deep Trenches Followed By Refilling The Same With Dielect
4,466,180	08/21/84	Method Of Manufacturing Punch Through Voltage Regulator Diodes Utilizing Shaping And Selective Doping
4,473,834		Light Emitting Transistor Array
4,477,962		Process For And Structure Of High Density VLSI Circuits, Having Self-Aligned Gates And Contacts For FET Devices And Conducting Lines
4,482,824		Tracking ROM Drive And Sense Circuit
4,484,143	11/20/84	CCD Demodulator Circuit
4,484,154	11/20/84	Frequency Control With A Phase-Locked-Loop
4,485,551		NPN Type Lateral Transistor Separated From Substrate By O.D.E. For Minimal Interference Therefrom And Method For Producing Same
4,488,266		Low-Power Address Decoder
4,493,819		Ceramic Strontium Ferrite Borate
4,495,473		Digital Phase Shifting Apparatus Which Compensates For Change Of Frequency Of An Input Signal To Be Phase Shifted
4,496,855		High Voltage Level Detector & Method
4,497,685		Small Area High Value Resistor With Greatly Reduced Parasitic Capacitance

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
4,498,156	02/05/85	Electrochromic Nonvolatile Memory Device
4,499,061		Strontium Ferrite Borate
4,503,345	03/05/85	MOS/LSI Time Delay Circuit
4,504,745		Clocked Tri-State Driver Circuit
4,506,283		Small Area High Value Resistor With Greatly Reduced Parasitic Capacitance
4,506,437		Process For And Structure Of High Density VLSI Circuits, Having Self-Aligned Gates And Contacts For FET Devices And Conducting Lines
4,507,741	03/26/85	Computing Device
4,507,852		Method For Making A Reliable Ohmic Contact Between Two Layers Of Integrated Circuit Metallizations
4,508,964		Electro-Optically Tuned Rejection Filter
4,509,095		Flexible Circuit Board Interconnect Having Low Thermal Conductivity
4,522,682		Method For Producing PNP Type Lateral Transistor Separated From Substrate By O.D.E. For Minimal Interference Therefrom
4,524,424	06/18/85	Adaptive Spectrum Shaping Filter
4,536,882	08/20/85	Embedded Absorber X-Ray Mask And Method For Making Same
4,538,235	08/27/85	Microcomputer Retriggerable Interval Counter
4,539,536	09/03/85	Parameter-Setting Approach To Obtain High Performance CTD Transversal Filters From Devices With Significant CTI And FPN
4,547,792		Selective Access Array Integrated Circuit
4,549,100		MOS Voltage Comparator And Method
4,550,331		Multilayer Modulation Doped Heterostructure Charge Coupled Device
4,552,740		Process For Producing Amorphous And Crystalline Silicon Nitride
4,554,505	11/19/85	Test Socket For A Leadless Chip Carrier
4,568,960		Blocked Impurity Band Detectors
4,571,538	02/18/86	Mask Alignment Measurement Structure For Semiconductor Fabrication
4,573,133	02/25/86	Signal Processor For Compensating Detector Non-Uniformities
4,575,821		Low Power High Speed Random Access Memory Circuit
4,580,331	04/08/86	PNP-Type Lateral Transistor With Minimal Substrate Operation Interference And Method For Producing Same
4,583,013		Oscillator signal Detect Circuit
4,584,762	04/29/86	Lateral Transistor Separated From Substrate By Intersecting Slots Filled With Substrate Oxide For Minimal Interference Therefrom And Method For Producing Same
4,586,074	04/29/86	Impurity Band Conduction Semiconductor Devices
4,587,711		Process For High Density VLSI Circuits, Having Self-Aligned Gates And Contacts For FET Devices And Conducting Lines
4,588,447		Method Of Eliminating P-Type Electrical Activity And Increasing Channel Mobility Of Si-Implanted And Recrystallized SOS Films
4,590,467		Local Area Network Interface Controller
4,593,281		Local Area Network Interframe Delay Controller
4,593,453		Two-Level transistor Structures And Method Utilizing Minimal Area Therefor
4,597,551		Polypropylene Holder For Vacuum Wands
4,598,270		Precision Integrating Analog-To-Digital Converter System
4,599,525		De-Glitch Circuitry For Video Game Memories
4,599,525		De-Glitch Circuitry For Video Game Memories

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
4,611,387		Process For Producing NPN Type Lateral Transistors
4,623,934	11/18/86	AGC Update Apparatus And Method Of Using Same
4,624,901		Intermediary Layers For Epitaxial Hexagon Ferrite Films
4,631,472		Isolated Low Power Thyristor Gate Drive Circuit
4,633,282		Metal-Semiconductor Field-Effect Transistor With A Partial P-Type Drain
4,639,142		Dimension Monitoring Technique For Semiconductor Fabrication
4,646,032		Controlled Oscillator Having Random Variable Frequency
4,654,587		Digital Peak Detector And Method Of Peak Detection
4,665,355	05/12/87	Off Line Capacitor-Divider Power Supply For Solid State Power Controller
4,670,090	06/02/87	Method For Producing A Field Effect Transistor
4,675,714		Gapless Gate Charge Coupled Device
4,676,867		Planarization Process For Double Metal MOS Using Spin-On Glass As A Sacrificial Layer
4,680,489	07/14/87	Controllable Piecewise Linear Gain Circuit
4,680,727	07/14/87	Complex Multiplier For Binary Two's Complement Numbers
4,683,399	07/28/87	Silicon Vacuum Electron Devices
4,684,903	08/04/87	Controlled Oscillator Having Random Variable Frequency
4,685,085		Non-Volatile Ram Cell With Charge Pumps
4,686,652	08/11/87	Non-Volatile RAM Cell With Single High Voltage Precharge
4,687,541		Dual Deposition Single Level Liftoff Process
4,694,565		Method Of Making Hardened CMOS Sub-Micron Field Effect Transistors
4,697,328		Method Of Making Hardened NMOS Sub-Micron Field Effect Transistors
4,698,417		Production Of Oxy-Metallo-Organic Polymer
4,701,422	10/20/87	Method Of Adjusting Threshold Voltage Subsequent To Fabrication Of Transistor
4,703,205		Uncompensated And Compensated Gallium Arsenide Input Receivers
4,705,962	11/10/87	Solid State Dc Rate Of Rise Controlled Switch
4,706,220		Non-Volatile RAM Cell With Dual High Voltage Precharge
4,706,244	11/10/87	Frequency Multiplexed Telephone System
4,707,808	11/17/87	Small Size High Speed Gaas Data Latch
4,709,315	11/25/87	Isolated Controller Circuit
4,731,339	03/15/88	Process For Manufacturing Metal-Semiconductor Field-Effect Transistors
4,731,340	05/15/88	Dual Lift-Off Self Aligning Process For Making Heterojunction Bipolar Transistors
4,731,340		Dual Lift-Off Self Aligning Process For Making Hbts
4,739,205	04/19/88	Telephone Signal Multiplexing System
4,740,386		Method For Depositing A Ternary Compound Having A Compositional Profile
4,740,414	04/26/88	Ceramic/Organic Multilayer Interconnection Board
4,740,883	04/26/88	Universal Solid State Power Controller
4,749,662	06/07/88	Diffused Field CMOS-Bulk Process
4,755,364	07/05/88	Liquid Phase Epitaxy Apparatus And Method
4,763,023		Clocked CMOS Bus Precharge Circuit Having Level Sensing
4,774,682		Nonlinear Statistical Signal Processor

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
4,775,882		Lateral Bipolar Transistor
4,787,058	11/22/88	Synthetic Quadrature Generating Apparatus
4,804,864		Multiphase CMOS Toggle Flip-Flop
4,807,008		Static Memory Cell Using A Heterostructure Complementary Transistor Switch
4,809,209	02/28/89	[Mybrid] Hybrid Charge-Transfer-Device Filter Structure
4,811,262	03/07/89	Distributed Arithmetic Realization Of Second-Order Normal-Form Digital Filter
4,815,851	03/28/89	Frequency Modulated Phase-Locked Stabilized Passive Ring Laser Gyro
4,828,648	5/9/89 L	liquid Phase Epitaxy Apparatus And Method
4,833,042		Nonalloyed Ohmic Contacts For N Type Gallium Arsenide
4,835,721		Frequency Synthesizer
4,835,791	05/30/89	Single Sideband Signal Generator
4,868,792	09/19/89	Difar Demultiplexer Test Circuit
4,873,685	10/10/889	Self-Checking Voting Logic For Fault Tolerant Computing Applications
4,878,179	10/31/89	Interactive Diagnostic Methodology And Apparatus For Microelectronic Devices
4,879,583		Diffused Field CMOS-Bulk Process And CMOS Transistors
4,879,694	11/7/89 D	Ifar Demultiplexer Circuit
4,896,202	01/23/90	Short Wavelength Impurity Band Conduction Detectors
4,908,524	03/13/90	High Voltage Pulse Power Converter
4,928,059	05/22/90	Sinusoidal Current Sense And Scaling Circuit
4,947,225		Sub-Micron Devices With Method For Forming Sub-Micron Contacts
4,962,304	10/09/90	Intrinsic Impurity Band Conduction Detectors
4,963,501		Method Of Fabricating Semiconductor Devices With Sub-Micron Linewidths
4,963,725		Adaptive Optical Neural Network
4,970,674	11/13/90	Programmable Windowing FFT Device With Reduced Memory Requirements
4,982,115	01/01/91	Digital Signal Direction Detection Circuit
4,990,983		Radiation Hardened Field Oxides For NMOS And CMOS-Bulk And Process For Forming
4,992,752	02/12/91	Method And Apparatus For Broadband Impedance Matching
4,994,407		Radiation Hardened Field Oxides For NMOS And CMOS Bulk And Process For Forming
4,996,165	02/26/91	Self-Aligned Dielectric Assisted Planarization Process
4,996,165		Self-Aligned Dielectric Assisted Planarization Process
5,002,824	03/26/91	Substrates Containing Electrically Conducting Coatings And Method Of Producing Same
5,004,308		Rugate Reflector
5,006,479		Methods For Improving Radiation Tolerance Of Silicon Gate CMOS/ Silicon On Sapphire Devices
5,006,824	04/09/91	Third Mode Torsional [F-M] E-M Resonator
5,010,340	04/23/91	Charge Domain Successive Approximation Analog To Digital Converter
5,014,057	05/07/91	Clockless A/D Converter
5,017,199	05/21/91	Spent Solvent Recovery In Circuit Processing

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
5,017,811		CMOS TTL Input Buffer Using A Ratioed Inverter With A Threshold Voltage Adjusted N Channel Field Effect Transistor
5,017,920	05/21/91	High-Speed Modified Successive Approximation Analog To Digital Converter
5,021,655	06/04/91	Apparatus And Method For Contactless Measurement Of Coating Thickness
5,025,302		PNP Type Lateral Transistor With Minimal Substrate Operation Interference
5,027,184	06/25/91	NPN Type Lateral Transistor With Minimal Substrate Operation Interference
5,028,549		Etched Back Edge Isolation Process For Heterojunction Bipolar Transistors
5,030,796		Reverse-Engineering Resistant Encapsulant For Microelectric Device
5,031,014		Lateral Transistor Separated From Substrate By Intersecting Slots Filled With Substrate Oxide For Minimal Interference Therefrom
5,033,019	07/16/91	Very-High-Speed Frequency-Domain FFT Windowing Device
5,041,191		Diffusion Barrier For Thin Film Hybrid Circuits
5,043,787		Extremely Small Area Npn Lateral Transistor
5,045,496	09/03/91	Semi-Insulating Cobalt Doped Indium Phosphide Grown By MOCVD
5,047,828		PNP Type Lateral Transistor With Minimal Substrate Operation Interference
5,051,805		Sub-Micron Bipolar Devices With Sub-Micron Contacts
5,051,869		Advanced Co-Fired Multichip/Hybrid Package
5,055,805	10/08/91	High Speed Polarization Switch Array For Selecting A Particular Orthogonal Polarization
5,056,118		Method And Apparatus For Clock And Data Recovery With High Jitter Tolerance
5,058,265		Method For Packaging A Board Of Electronic Components
5,072,314	12/10/91	Image Enhancement Techniques Using Selective Amplification Of Spatial Frequency Components
5,091,288	02/25/92	Method Of Forming Detector Array Contact Bumps For Improved Lift Off Of Excess Metal
5,093,801	03/03/92	Arrayable Modular FFT Processor
5,095,525	03/10/92	Memory Transformation Apparatus And Method
5,097,121	03/17/92	Beveled Edge Impurity Band Conduction Detector
5,097,316		Complementary NPN And PNP Lateral Transistors Separated From Substrate By Intersecting Slots Filled With Substrate Oxide For Minimal Interf
5,103,183	04/07/92	Method Of Profiling Compensator Concentration In Semiconductors
5,113,399	05/12/92	Memory Test Methodology
5,114,867		Sub-Micron Bipolar Devices With Method For Forming Sub-Micron Contacts
5,114,874		Method Of Making A Sub-Micron NMOS, PMOS And CMOS Devices With Methods For Forming Sub-Micron Contacts
5,121,035		System Which Cooperatively Uses A Systolic Array Processor And Auxiliary Processor For Pixel Signal Enhancement
5,123,164	06/23/92	Hermetic Organic/Inorganic Interconnection Substrate For Hybrid Circuit Manufacture
5,124,578		Receiver Designed With Large Output Drive And Having Unique Input Protection Circuit

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
5,125,111		Resistive Planar Ring Double-Balanced Mixer
5,132,648		Large Array MMIC Feedthrough
5,137,544	08/11/92	Stress-Free Chemo-Mechanical Polishing Agent For II-VI Compound Semiconductor Single Crystals And Method Of Polishing
5,147,740		Analog Maximum/Minimum Selector Circuit
5,151,389		Method For Dicing Semiconductor Substrates Using An Excimer Laser Beam
5,157,876	10/27/92	Stress-Free Chemo-Mechanical Polishing Agent For II-VI Compound Semiconductor Single Crystals And Method Of Polishing
5,175,605	12/29/92	Single Event Upset Hardening Circuits Devices And Methods
5,179,380	01/12/93	One-Bit Sigma-Delta Modulator With Improved Signal Stability
5,180,641		Battery Cell Bypass Circuit
5,185,274		HBT Differential Pair Chip For Quasi-Optic Amplifiers
5,202,283	04/13/93	Technique For Doping MOCVD Grown Crystalline Materials Using Free Radical Transport Of The Dopant Species
5,202,966	04/13/93	Centralized Bus Arbitration Circuit
5,214,261		Method And Apparatus For Dicing Semiconductor Substrates Using An Excimer Laser Beam
5,218,440		Vehicle Lane Position Detection System
5,219,713	06/15/93	Multi-Layer Photoresist Air Bridge Fabrication Method
5,233,551	08/03/93	Radix-12 DFT/FFT Building Block
5,247,515	09/21/93	Apparatus For Extracting One From Many Multiplexed Signals
5,250,826		HBT With Semiconductor Ballasting
5,265,266		Resistive Planar Star Double-Balanced Mixer
5,266,819		Electronically Scanned Buffered Direct Injection Circuit For Staring IR Focal Plane Array
5,270,953	12/14/93	Fast Convolution Multiplier
5,274,739	12/28/93	Product Code Memory Itakura-Saito (MIS) Measure For Sound Recognition
5,293,085		Gaas Driver Circuit
5,304,864		Method Of Transferring A Thin Film To An Alternate Substrate
5,306,660	04/26/94	Technique For Doping Mercury Cadmium Telluride MOCVD Grown Crystalline Materials Using Free Radical Transport Of Elemental Indium And Apparatus Therefor
5,315,651		Active Surge Rejection Circuit
5,317,173		Flexible Rejection Filter (U)
5,319,587	06/07/94	Computing Element For Neural Networks
5,323,339	06/21/94	Digital Anti-Aliasing Filter System
5,337,016	08/09/94	Method And Apparatus For Traveling Wave Attenuation Measurement
5,337,056	08/09/94	Dynamically Tunable Notch Filter
5,337,257	08/09/94	Moving Vehicle Classifier With Nonlinear Mapper
5,339,263	09/16/94	Combined Decimation/Interpolation Filter For ADC And DAC
5,361,036	11/01/94	Complex Digital Demodulator Employing Chebychev-Approximation Derived Synthetic Sinusoid Generation
5,361,379	11/01/94	Soft-Decision Classifier
5,365,460		Neural Network Signal Processor
5,366,934	11/22/94	Method For Sulfide Surface Passivation
5,367,475	11/22/94	Moving Vehicle Classifier With Iterative Deconvolver Estimator
5,369,654	11/29/94	Fault Tolerant Gate Array Using Duplication Only
5,369,666		Modem With Digital Isolation

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
5,378,922		Deposition Of Multiple Layer Thin Films Using A Broadband Spectral Monitor
5,386,513	01/31/95	Self Filling And Emptying Data Pipeline Tengan
5,396,368		Object-Background Discrimination Using Analog Vlsi Circuit
5,399,989	03/21/95	Voltage Amplifying Source Follower Circuit
5,404,306	04/04/95	Vehicular Traffic Monitoring System
5,422,924	06/06/95	Gain And Offset Control For Charge Based Signals
5,428,361	06/27/95	Large Time-Bandwidth Chirp Pulse Generator
5,437,739	08/01/95	Etch Control Seal For Dissolved Wafer Micromachining Process
5,440,162		ESD Protection For Submicron CMOS Circuits
5,440,239		Transferable Solder Bumps For Interconnect And Assembly Of MCM Substrates
5,444,641	08/22/95	Admittance-Parameter Estimator For A Piezoelectric Resonator In An Oscillator Circuit
5,450,530		High Speed Receiver/Transmitter Interface
5,454,015		Adaptive Timing Recovery With Gain Adjustment
5,455,542		Symmetrical Clock Crystal Oscillator Circuit
5,457,455		Real Time Keyboard Scanner
5,457,459	10/10/95	Analog To Digital Converter For Charge Coupled Signals
5,459,432	10/17/95	Use Of A Chopper And A Sigma-Delta Modulator For Downconverting And Digitizing An Analog Signal Including Information Modulated By A Carrier
5,459,762		Variable Multi-Threshold Detection For 0.3-GMSK
5,463,336		Supply Sensing Power-On Reset Circuit
5,463,575	10/31/95	Reduced Quantization Noise From Single-Precision Multiplier
5,463,633		Modem Tester
5,471,396	11/28/95	Estimator Of Amplitude And Frequency Of A Noisy-Biased Sinusoid From Short Bursts Of Samples
5,473,754		Branch Decision Encoding Scheme
5,479,626		Signal Processor Contexts With Elemental And Reserved Group Addressing
5,481,223		Bi-Directional Spatial Power Combiner Grid Amplifier
5,481,606		Opto-Coupling Device Between A Modem And A Telephone Line
5,486,795		Low Power Crystal Oscillator
5,487,015	01/23/96	Self-Oscillating Driver Circuit For A Quartz Resonator Of An Angular Rate Sensor
5,487,673		Package, Socket, And Connector For Integrated Circuit
5,491,725	02/13/96	Tracking Filter And Quadrature-Phase Reference Generator
5,504,482	04/02/96	Automobile Navigation Guidance, Control And Safety System
5,509,974		Etch Control Seal For Dissolved Wafer Process
5,514,951		Supply-Discriminating Supply-Adaptive Electronic System
5,514,985		Virtual Amplifier
5,517,530		Method And Apparatus For Dual Demodulation Of Mobile Channel Signals
5,521,539		Delay Line Providing An Adjustable Delay
5,523,719		Analog Bandpass Filter
5,530,342		Micromachined Rate Sensor Comb Drive Device And Method
5,530,398		Reference Voltage Generation For IC
5,532,576	07/02/96	Efficient, Well Regulated, DC-DC Power Supply Up-Converter For CMOS Integrated Circuits

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
5,541,598		Current Cell For Converting A Binary Value To An Analog Value
5,541,990		Compensated Hybrid
5,542,041		Apparatus For, And Methods Of, Providing A Universal Format Of Pixels And For Scaling Fields In The Pixels
5,544,222		
5,544,311		On-Chip Debug System With Picture Claim
5,550,862		Stabilizing Equalizer Taps
5,550,866	08/27/96	Digital Demodulator Reference Signal Generator Having DC Blocker And First Hilbert Transformation With Quadrature Output Followed By Gain Staging And Combination For Second Hilbert Transformation Quadrature Output
5,552,794		Position Estimation Using Satellite Range Rate Measurements
5,554,950		Delay Line Providing An Adjustable Delay In Response To Binary Input Signals
5,566,093	10/15/96	Sensor With Resonator, Digital Filter, And Display
5,574,452		Bandpass A/D With Analog Filter
5,574,750		Methods And Apparatus For Detecting A Cellular Digital Packet Data (CDPD) Carrier
5,576,976	11/19/96	Amplitude Detection And Automatic Gain Control Of A Sparsely Sampled Sinusoid By Adjustment Of A Notch Filter
5,577,073	11/19/96	Frequency And Phase-Locked Two-Phase Digital Synthesizer
5,586,284		Risc Data Processor
5,592,382		Directional Steering And Navigation Indicator
5,606,732		Direct Connect Radio And Antenna Assembly
5,612,996		Loop Gain Processing System For Speakerphone Applications
5,614,864	03/25/97	Single-Ended To Differential Converter With Relaxed Common-Mode Input Requirements
5,617,063		Matched Filters For Processing Related Signal Components
5,627,112		Method Of Making Suspended Microstructures
5,627,499		Digital Modulator And Upconverter Having Single-Bit Delta-Sigma Data Converters
5,627,986		Device And Method For Selecting And Addressing Extended Memory Addresses
5,631,603		Offset Cancellation For Differential Amplifiers
5,638,077	06/10/97	Differential GPS For Fleet Base Stations With Vector Processing Mechanization
5,646,990		Efficient Speakerphone Anti-Howling System
5,648,992	07/15/97	Multiple Antenna Home Base For Digital Cordless Telephones
5,654,862		Method And Apparatus For Coupling Multiple Independent On-Chip VDD Busses To An ESD Core Clamp
5,655,132		Register File With Multi-Tasking Support
5,661,762	08/26/97	Multiple Antenna Home Base For Digital Cordless Telephones
5,664,054		Spike Code-Excited Linear Prediction
5,686,847		Reduced Sensitivity Power-On Reset Circuitry
5,687,163		Method And Apparatus For Signal Classification Using I/Q Quadrant Histogram
5,689,615		Usage Of Voice Activity Detection For Efficient Coding Of Speech
5,694,521		Variable Speed Playback System
5,696,760		Temporary Equipment Identifier Message Response Control Method
5,696,798	12/09/97	Multiple Antenna Home Base For Digital Cordless Telephones

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
5,699,481		Timing Recovery Scheme For Packet Speech In Multiplexing Environment Of Voice With Data Applications
5,706,369		Base-N Resolution Converter
5,712,860		Methods And System For Using Multi-Block Bursts In Half Duplex Subscriber Unit Transmissions
5,717,824		Adaptive Speech Coder Having Code Excited Linear Predictor With Multiple Codebook Searches
5,719,902		Methods And Apparatus For Detecting Cellular Digital Packet Data (CDPD)
5,724,657	03/03/98	Adaptive Frequency Correction Burst Detector For GSM Handset System
5,729,557		Cellular Communication System With Multiple Code Rates
5,732,111		Frequency Error Compensation For Direct Sequence Spread Spectrum Systems
5,736,887		Five Volt Tolerant Protection Circuit
5,742,679		Optimized Simultaneous Audio And Data Transmission Using QADM With Phase Randomization
5,754,601		Jitter Circuit For Reduced Switching Noise
5,757,864	05/26/98	Receiver With Filters Offset Correction
5,758,263		Selection Of Communication Channel In A Digital Cordless Telephone
5,761,250		Iterative Filtering Frequency Estimator And Estimation Method
5,764,689		Variable Digital Automatic Gain Control In A Cordless Direct Sequence Spread Spectrum Telephone
5,764,689		Variable Digital Automatic Gain Control in a Cordless Direct Sequence Spread Spectrum Telephone
5,764,694		Self-Testing And Evaluation System
5,768,309		Unified Trellis Encoder
5,774,839		Delayed Decision Switched Prediction Multi-Stage LSF Vector Quantization
5,774,849		Method And Apparatus For Generating Frame Voicing Decisions Of An Incoming Speech Signal
5,778,022		Extended Time Tracking And Peak Energy In-Window Demodulation For Use In A Direct Sequence Spread Spectrum System
5,778,241		Space Vector Data Path
5,781,128		Data Compression System And Method
5,781,880		Pitch Lag Estimation Using Frequency-Domain Lowpass Filtering Of The Linear Predictive Coding (LPC) Residual
5,790,561	08/04/98	Internal Testability System For Microprocessor-Based Integrated Circuit
5,790,656	08/04/98	Data Access Arrangement With Telephone Interface
5,799,034	08/25/98	Frequency Acquisition Method For Direct Sequence Spread Spectrum Systems
5,811,706	09/22/98	Synthesizer System Utilizing Mass Storage Devices For Real Time, Low Latency Access Of Musical Instrument Digital Samples
5,818,463		Data Compression For Animated Three Dimensional Objects
5,821,785	10/13/98	Clock Signal Frequency Multiplier
5,828,692	10/27/98	Baseband Demodulator For Polar Or Rectangular Modulated Signal In A Cordless Spread Spectrum Telephone

Schedule of Contributed Inventions and Patents

United States

Patent No.	Issue Date	Title
5,828,708	10/27/98	CID Demodulator For DCT