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its and Trademarks:

ATTN. BOX ASSIGNMENTS

----- are attached original documents or copy thereof.

 1. Name of conveying party(ies):  
Yasuyuki NOZUYAMA

2. Name and address of receiving party(ies):

Name: KABUSHIKI KAISHA TOSHIBA

 Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

Internal Address:

3. Nature of conveyance:

 Street Address: 1-1, Shibaura 1-chome, Minato-ku,  
Tokyo, Japan

☒ Assignment ☐ Merger

City:

☐ Security Agreement ☐ Change of Name

State:

Zip Code:

☐ Other:

Additional name(s) &amp; Address(es) attached?

Execution Date: September 19, 2001

☐ Yes

☒ No

4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application: September 19, 2001

A. Patent Application Number(s):

B. Patent Number(s):

Additional numbers attached?

☐ Yes

☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Mr. Ernest F. Chapman

 6. Total number of applications and registrations involved:  
1

7. Total fee (37 CFR 3.41): \$40

☒ Enclosed (Please charge deficiency to deposit account)

☐ Authorized to be charged to deposit account

 Internal Address: FINNEGAN, HENDERSON, FARABOW, GARRETT  
& DUNNER, L.L.P.

Street Address: 1300 I Street, N.W.

City: Washington, D.C.

State: Zip: 20005-3315

8. Deposit Account No.: 06-0916

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

 Richard V. Burgujian  
Reg. No. 31,744

Signature

September 24, 2001

Date

Total number of pages including cover sheet, attachments and documents: 2

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**PATENT  
REEL: 012201 FRAME: 0786**

A S S I G N M E N T

WHEREAS I/We, the below named inventor(s), [hereinafter referred to as Assignor(s)], have made an invention entitled: SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING A TEST FACILITATION CIRCUIT FOR FUNCTIONAL BLOCKS INTELLECTUAL PROPERTIES AND AUTOMATIC INSERTION METHOD OF THE SAME TEST FACILITATION CIRCUIT for which I/WE executed an application for United States Letters Patent concurrently herewith or on \_\_\_\_\_ or filed an application for United States Letters Patent on \_\_\_\_\_, (Serial No. \_\_\_\_\_); and

WHEREAS, KABUSHIKI KAISHA TOSHIBA  
a corporation of Japan, whose post office  
address is 1-1, Shibaura 1-chome, Minato-ku, Tokyo, Japan  
(hereinafter referred to as Assignee), is desirous of securing the entire right, title, and interest in and to this invention, the application for United States Letters Patent on this invention and the Letters Patent to be issued upon this application;

NOW THEREFORE, be it known that, for good and valuable consideration the receipt of which from assignee is hereby acknowledged, I/WE, as assignor(s), have sold, assigned, transferred, and set over, and do hereby sell, assign, transfer, and set over unto the assignee, its lawful successors and assigns, my/our entire right, title, and interest in and to this invention and this application, and all divisions, and continuations thereof, and all Letters Patent of the United States which may be granted thereon, and all reissues thereof; and I/WE hereby authorize and request the Commissioner of Patents and Trademarks of the United States to issue all Letters Patent for this invention to assignee, its successors and assigns, in accordance with the terms of this Assignment;

AND, I/WE HEREBY further covenant and agree that I/We will, without further consideration, communicate with assignee, its successors and assigns, any facts known to me/us respecting this invention and testify in any legal proceeding, sign all lawful papers when called upon to do so, execute and deliver all papers that may be necessary or desirable to perfect the title to this invention in said assignee, its successors and assigns, execute all divisional, continuation, and reissue applications, make all rightful oaths and generally do everything possible to aid assignee, its successors and assigns, to obtain and enforce proper patent protection for this invention in the United States, it being understood that any expense incident to the execution of such papers shall be borne by the assignee, its successors and assigns.

AND, I/WE HEREBY authorize and request the attorneys I/we have empowered in the Declaration and Power of Attorney in this application, to insert here in parentheses (Application No. \_\_\_\_\_, filed \_\_\_\_\_) the filing date and application number of said application when known.

IN TESTIMONY WHEREOF, I/We have hereunto set our hand(s).

1. Full Name of Sole or First Assignor	: Assignor's Signature	: Date
<u>Yasuyuki NOZUYAMA</u>	: <u>Yasuyuki Nozuyama</u>	<u>September 19, 2001</u>
Address		: Citizenship
<u>Tokyo, Japan</u>		: <u>Japan</u>
2. Full Name of Second Assignor	: Assignor's Signature	: Date
	:	:
Address		: Citizenship
		:
3. Full Name of Third Assignor	: Assignor's Signature	: Date
	:	:
Address		: Citizenship
		:

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P. · WASHINGTON, D.C.