

10-24-2001

IN THE



TRADEMARK OFFICE

Docket No.: NSC-P04879

101883617

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|--|----------|--|-------------------|
| I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Assignment Branch, North Tower Building, Suite 10C35, Washington, D.C., 20231, on the below date of deposit. | | | |
| Date of Deposit: | 10/09/01 | Name of Person Making the Deposit: | KATHERINE RINALDI |
| | | Signature of the Person Making the Deposit: <i>Katherine Rinaldi</i> | |

The Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Please record the attached original document(s) or copy(ies) thereof:

10-15-01

Transmittal of an Assignment

1. Name of conveying party(ies): Siew Siong Teo, Michael Eugene Broach and Richard Frank

Serial No.: 09/881,933

Group Art Unit:

Filed: 06/14/01

Examiner:

2. Name and Address of Receiving party(ies): NATIONAL SEMICONDUCTOR CORPORATION
2900 Semiconductor Drive, Santa Clara, CA 95051

3. Nature of Conveyance: Assignment
Execution Date: 08/28/01, 09/07/01, 9/25/01

4. New Patent Application entitled: A PRECISION LOW VOLTAGE SUPPLY SYSTEM AND METHOD WITH UNDERVOLTAGE LOCKOUT CAPABILITIES

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: John F. Ryan
Address: Wagner, Murabito & Hao LLP
Two North Market Street, Third Floor, San Jose, CA 95113
Phone: (408) 938-9060

6. Total Number of applications and patents involved: ONE

7. Fee Calculation (for other than a small entity)

Assignment Recordation Fee, per property 1 X \$40.00
Total Fees (37 CFR 3.41) \$40.00

X The amount of \$ 40.00 is enclosed

8. At any time during the pendency of this application, please charge any additional fees required or credit any overpayments to Deposit Account 23-0085. A duplicate copy of this transmittal is enclosed.

9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

RECORD TOTAL: 40.00
40.00

Date: October 9, 2001

John F. Ryan
Name John F. Ryan
Reg. No. 47,050

Assignment to National Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Siew Siong Teo

do hereby sell, assign, and transfer unto National Semiconductor Corporation (hereinafter called National Semiconductor), a Delaware Corporation having its principal place of business at 2900 Semiconductor Drive, Santa Clara, CA 95051, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

A PRECISION LOW VOLTAGE SUPPLY SYSTEM AND METHOD WITH UNDERVOLTAGE LOCKOUT CAPABILITIES

filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

Serial No.: 09/881,933 filed on 06/14/01 and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;

And for the above consideration, I/we agree promptly upon request of National Semiconductor, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to National Semiconductor, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by National Semiconductor.

I/we further covenant with National Semiconductor, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature:  Date: Aug. 28, 2001

Assignment to National Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Richard Frank

do hereby sell, assign, and transfer unto National Semiconductor Corporation (hereinafter called National Semiconductor), a Delaware Corporation having its principal place of business at 2900 Semiconductor Drive, Santa Clara, CA 95051, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

A PRECISION LOW VOLTAGE SUPPLY SYSTEM AND METHOD WITH UNDERVOLTAGE LOCKOUT CAPABILITIES

filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

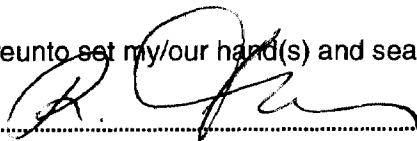
Serial No.: 09/881,933 filed on 06/14/01 and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;

And for the above consideration, I/we agree promptly upon request of National Semiconductor, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to National Semiconductor, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by National Semiconductor.

I/we further covenant with National Semiconductor, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature: _____



Date: _____

SEPT 25/01