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HEET

U.S. DEPARTMENT OF COMMERCE

Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks: .... .tached original documents or copy thereof.

1. Name of conveying party(ies):	2. Name and address of receiving party(ies):

1. Name of conveying party(ies):

Manjunath D. Haritsa
Manishkumar B. Ankola
Ralf M. Schmitt   *10-17-01*
Anup Sharma
Stephan Hoerold
David M. Murata

Additional name(s) of conveying parties attached? ___ Yes   X No

2. Name and address of receiving party(ies):

Name _____ Sun Microsystems, Inc. _____

Internal Address _____

Street Address ___ 901 San Antonio Road

City __Palo Alto__ State ____CA____ Zip 94303

Additional name(s) & address(es) attached? ___ Yes _X_ No

3. Nature of conveyance:

   X Assignment   ☐ Merger

   ☐ Security Agreement   ☐ Change of Name

   ☐ Other _____

Execution Date _9/21/01; 10/7/01; 10/8/01; 10/11/01_

4. Application number(s) or patent number(s):   *09/982452*

If this document is being filed together with a new application, the execution date of the application is ___October___ , 2001
A. Patent Application No. (s)   B. Patent No. (s)

Additional numbers attached? ☐ Yes   X No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name ____ Thelen Reid & Priest LLP

Internal Address _____

_____

Street Address ___ P. O. Box 640640

City ____San Jose____ State __CA__ Zip __95164-0640__

6. Total number of applications and patents involved **1**

7. Total fee (37 CFR §3.41) .... $ ___40.00___

   X Enclosed

   X Authorized to be charged to deposit account

8. Deposit account number:

                    50-1698

(Attach duplicate copy of this page if paying by deposit account)

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9. Statement and signature
   To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.
   ___Diane Morse_____   X ~~Diane Morse~~   _10/17/01_
                              Signature   Date

Total number of pages including cover sheet, attachments and document **7**

Mail documents to be recorded with required cover sheet information to:
Commissioner of Patents & Trademarks, Box Assignments, Washington, DC 20231

PATENT
REEL: 012281 FRAME: 0876

ASSIGNMENT

WHEREAS, we, Manjunath D. Haritsa, a citizen of India, residing at 1031 Pilinut Court, Sunnyvale, CA 94087, Manishkumar B. Ankola, a citizen of India, residing at 2258 Lenox Place, Santa Clara, CA 95054, Ralf Schmitt, a citizen of Germany, residing at 160 Cottonwood Court, Mountain View, CA 94043, Anup Sharma, a citizen of India, residing at 3755 Terstena Pl. #177, Santa Clara, CA 95051, Stephan Hoerold, a citizen of Germany, residing at 955 Escalon Avenue, #811, Sunnyvale, CA 94085, and David Minoru Murata, a citizen of the United States of America, residing at 20954 Pepper Tree Lane, Cupertino, CA 95014, have invented a "**Clock Skew Verification Methodology for Grid-Based Design**" for which we have executed application papers for a U.S. patent thereon which is being filed concurrently herewith; and

WHEREAS, Sun Microsystems, Inc., a California corporation, having a place of business located at 901 San Antonio Road, Palo Alto, California 94303, is desirous of acquiring the exclusive right, title and interest in and to said invention and in and to the Letters Patent to be granted and issued therefor in the United States of America and its territories and possessions, and all countries foreign thereto;

NOW, THEREFORE, for a valuable consideration, the receipt of which is hereby acknowledged, we, Manjunath D. Haritsa, Manishkumar B. Ankola, Ralf Schmitt, Anup Sharma, Stephan Hoerold, and David M. Murata, do sell, assign, transfer and set over unto the said Sun Microsystems, Inc., its successors and assigns, the full and exclusive right, title and interest in and to said invention, and in and to any and all Letters Patent to be granted and issued therefor, not only for, to and in the United States of America, its territories and possessions, but also for, to and in all other countries including all priority rights under the International Convention; and we hereby authorize and request the Commissioner of Patents and Trademarks to issue said Letters Patent to said Sun Microsystems, Inc., its successors and assigns, in accordance with this Assignment.

ASSIGNMENT CONTINUATION

Re: U.S. Patent Application entitled: Clock Skew Verification Methodology for Grid-Based Design
Inventor: Manjunath D. Haritsa
Filing date: Herewith

WITNESS MY HAND at ___Sunnvale___, California, this __21st__ day of
__September.___, 2001.

_____
Manjunath D. Haritsa

ASSIGNMENT CONTINUATION

Re: U.S. Patent Application entitled: Clock Skew Verification Methodology for Grid–Based Design
Inventor: (name) Manishkumar B. Ankola
Filing date: Herewith

WITNESS MY HAND at _Sunnyvale_ , California, this _7th_ day of
October , 2001.

MAnk
Manishkumar B. Ankola

ASSIGNMENT CONTINUATION

Re: U.S. Patent Application entitled: Clock Skew Verification Methodology for Grid–Based Design
Inventor: (name) Ralf Schmitt
Filing date: Herewith

WITNESS MY HAND at _Sunnyvale_, California, this _11_ day of
October, 2001.

Ralf Schmitt
Ralf M. Schmitt

ASSIGNMENT CONTINUATION

Re: U.S. Patent Application entitled: Clock Skew Verification Methodology for Grid–Based Design
Inventor: (name) Anup Sharma
Filing date: Herewith

WITNESS MY HAND at _Sunnyvale_ , California, this __8__ day of _October_ , 2001.

Anup Sharma (signature)
Anup Sharma

ASSIGNMENT CONTINUATION

Re: U.S. Patent Application entitled: Clock Skew Verification Methodology for Grid–Based Design
Inventor: (name) Stephan Hoerold
Filing date: Herewith

WITNESS MY HAND at ___Sunnyvale___, California, this __21__ day of
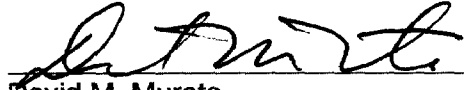_____, 2001.

Stephan Hoerold

ASSIGNMENT CONTINUATION

Re: U.S. Patent Application entitled: Clock Skew Verification Methodology for Grid-Based Design
Inventor: (name) David M. Murata
Filing date: Herewith

WITNESS MY HAND at _Sunnyvale_____, California, this __11th__ day of
_October_____, 2001.

David M. Murata