

.1/28/01

Express Mailing Label No. ET662218715US

**DOCKET NO. 01-376** 

**PATENT** 



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Roger Young, Ann Kang, and Bruce Whitefield

Assignee:

LSI Logic Corporation

Title:

PROCESS FOR INHIBITING EDGE PEELING OF COATING ON

SEMICONDUCTOR SUBSTRATE DURING FORMATION OF

INTEGRATED CIRCUIT STRUCTURE THEREON

Serial No.:

Unknown

Filed:

Herewith

Group Art Unit: Unknown

Attorney Docket No. 01-376

Examiner: Unknown

09/997071

November 28, 2001

## RECORDATION FORM COVER SHEET PATENTS ONLY

To the Honorable Commissioner of Patents and Trademarks Washington, DC 20231

Sir:

Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Roger Young

Date of Execution: November 26, 2001

Ann Kang

Date of Execution: November 26, 2001

Bruce Whitefield

1

Date of Execution: November 27, 2001

12/06/2001 LMUELLER 00000142 122252 09997071

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**PATENT** 

REEL: 012337 FRAME: 0921

2.	Name and address of receiving party(ies):			
	LSI Logic Corporation 1551 McCarthy Boulevard Milpitas, California 95035 U.S.A.			
3.	Nature of conveyance: Assignment			
4.	Application number(s) or patent number(s):  If this document is being filed together with a new application, the execution date of the application is:  November 26, 2001, November 26, 2001, and November 27, 2001.			
	A. Patent Application Serial Number:  B. Patent Number:			
5.	Name and address of party to whom correspondence concerning document should be mailed:			
	Sandeep Jaggi, Chief Intellectual Property Counsel Intellectual Property Law Department LSI Logic Corporation M/S D-106 1551 McCarthy Boulevard Milpitas, CA 95035 U.S.A.			
6.	Total number of applications and patents involved: 1.			
7.	Total fee (37 CFR 3.41): \$40.00.			
8.	Attached is a check in the amount of \$			
	Charge Deposit Account No. 12-2252 in the amount of \$ 40.00. (A duplicate copy of this document is enclosed.)			
9.	Statement and Signature: To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.			
	John P. Taylor Reg. No. 22,369			
	John P. Taylor Reg. No. 22,369			
10.	Total number of pages including cover sheet, attachments and document:5			

2

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Docket No.: 01-376 Patent

which has been executed by the undersigned concurrently herewith,

## ASSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/WE, the undersigned, Roger Young, Ann Kang, and Bruce Whitefield\_, do hereby sell, assign, and transfer to: LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at 1551 McCarthy Boulevard, Milpitas, California 95035, ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements which are disclosed in the application for United States Letters Patent,

which was filed on \_\_\_\_\_\_ 20\_\_\_ and assigned Serial No. \_\_\_\_\_.

FORMATION OF INTEGRATED continuing, substitute, renewal in the United States and all forest	O CIRCUIT STRUCTURE T al, reissue, and all other ap eign countries on any of sa ssued in the United States	"HEREON" and in a plications for Letters id improvements; and and all foreign countries."	SEMICONDUCTOR SUBSTRATE DURING and to said application and all divisional, as Patent which have been or shall be filed in and to all original and reissued patents tries on said improvements; and in and to and
when requested, without ch representatives, to carry out in divisional, continuing, substitu execute all rightful oaths, ass successors, assigns, and repre- history thereof; and generally di- consider desirable for aiding in	arge to, but at the exp or good faith the intent and ate, renewal, reissue, and a signments, powers of att esentatives, all facts know o everything possible which securing and maintaining p d all applications to patent	ense of, said Assig purpose of this Ass all other patent applic orney and other par vn to the undersigne h said Assignee, its so proper patent protect	mprovements in its own name; and that, gnee, its successors, assigns and legal ignment, the undersigned will execute all cations on any and all said improvements; coers; communicate to said Assignee, its ed relating to said improvements and the uccessors, assigns or representatives shall tion for said improvements and for vesting said improvements, in said Assignee, its
	affecting the rights and p	property herein conv	ves that no assignment, grant mortgage, veyed has been made to others by the ossessed by the undersigned.
Name:	Date:	/ <del>26</del> , 2001	Witness: Nyayalakuhi Venkelti an
Roger Young			Witness: VIJA YALAKOMI Printed VENKATEGAN
Name: Un Muy	Date: 11/2	<u>6</u> , <sub>2001</sub>	Witness:
Ann Kang	$\mathcal{A}$		Witness: Brent Guyan Printed
Name: bue white	Date: 11/27/0	(, 2001	Witness: John Fan
Bruce Whitefield	,		Witness: John Knah
Name:	Date:	, 2001	Witness:

Assignment (Rev. 111494)

( )

RECORDED: 11/28/2001 REEL: 0

Witness: Printed