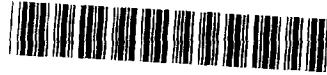


01-02-2002



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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original document or copy thereof.

1. Name(s) of conveying party(ies):

1) CEM BASCERI
 2) F. DANIEL GEALY
 3)
 4)
 5)
 6)

12/13/01

Additional names of conveying parties attached? Yes No

2. Name and address of receiving party:

Name: MICRON TECHNOLOGY, INC.

Internal Address: _____

Street Address: 8000 SOUTH FEDERAL WAY
P.O. BOX 6 / 83707-0006

City: BOISE State IDAHO

Zip: 83716-9632

Additional names & addresses attached? Yes No

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3. Nature of conveyance:

Assignment Merger

Security Agreement Change of Name

Other _____

Execution Dates:

1) NOVEMBER 28, 2001 4) _____
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4. Application number(s) or registration number(s):

If this document is being filed together with a new application, the execution date of the application is DECEMBER 13, 2001.

A. Patent Application No(s). B. Patent No(s).

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: DORSEY & WHITNEY LLP

Internal Address: KIMTON N. ENG
SUITE 3400

Street Address: 1420 FIFTH AVENUE

City: SEATTLE State: WA ZIP: 98101

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6. Total number of applications and patents involved..... One (1)

7. Total Fee (37 CFR 3.41): \$40

Enclosed

Authorized to be charged to deposit account

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50-1266

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9. Statement and signature.

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 Name of Person Signing Signature Date

Total number of pages including cover sheet, attachments, and document: 4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Cem Basceri and Daniel F. Gealy

Docket No. 501086.01

Disclosure No. 01-0445

Filed : Concurrently herewith

For : SYSTEM AND METHOD FOR
INHIBITING IMPRINTING OF
CAPACITOR STRUCTURES OF A
MEMORY

A S S I G N M E N T :

 Enclosed for recording Previously recorded

Date: _____

Reel: _____

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned do hereby:

SELL, ASSIGN AND TRANSFER to **Micron Technology, Inc.** (the "Assignee"), a corporation of Delaware, having a place of business at 8000 South Federal Way, Boise, Idaho 83716-9632, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith and is entitled "**SYSTEM AND METHOD FOR INHIBITING IMPRINTING OF CAPACITOR STRUCTURES OF A MEMORY**"; such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States of America;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

