FORM PTO-1619A Expires 06/30/99 OMB 0651-0027



U.S. Department of Commerce Patent and Trademark Office **PATENT**

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Name (line 1)	KIM, Jong Heon		Month Day Year		
Name (line 2)			Execution Date		
Second Party Name (line 1)		1895	Month Day Year		
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	4.2	Mark if a	dditional names of receiving parties attached		
Receiving Par			If document to be recorded		
Name (line 1)	HYNIX SEMICONDUCTOR INC		is an assignment and the receiving party is not		
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Address (line 1)	San 136-1, Ami-ri, Buba	1-eub, Ich'on	representative is attached. (Designation must be a separate document from		
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Correspondent Name and Address Area Code and Telephone Number (312) 427-1300				
Name [Richard J. Streit			
Address (line 1)	Ladas & Parry			
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If this document is being filed together with a <u>new</u> Patent Application, enter the date the patent application was signed by the first named executing inventor. Month Day Year 11 29 01				
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has r	not been assigned.	roi roi		
Number of Properties Enter the total number of properties involved. # 1				
Fee Amount	Fee Amount fo	r Properties Listed (37 CFR 3.41):	\$ 40.00	
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	payment by deposit account or if addition	onal fees can be charged to the account.) posit Account Number:	# 12-0400	
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To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Charges to deposit account are authorized, as indicated herein.				
	Economou	Vangeli Erono	December 18, 2001	
Name Reg. 323	of Person Signing	Signature	Date	

PATENT REEL: 012406 FRAME: 0981

UNITED STATES OF AMERICA ASSIGNMENT

In consideration of the payment of ASSIGNEE to ASSIGNOR of the sum of One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for the other good and valuable consideration,

ASSIGNOR:

Name: Jong Heon KIM

Address: 101-1107, Daewon Apt., Kakyoung-dong, Heungdeok-gu, Cheongju,

Chungcheongbuk-do, Korea

(INVENTOR NAME AND ADDRESS)

hereby sells, assigns and transfers to

ASSIGNEE:

Name: Hynix Semiconductor Inc.

Address: San 136-1, Ami-ri, Bubal-ebu, Ich'on, Kyoungki-do, Korea

(ASSIGNEE NAME AND ADDRESS)

and the successors, assigns and legal representatives of the ASSIGNEE the entire right, title and interest for the United States and its territorial possessions in and to, any and all improvements which are disclosed in the invention entitled:

METHOD OF FABRICATING A WAFER LEVEL PACKAGE

(TITLE)

and which is found in (check one applicable item below)

X U.S. patent application execu	ted on even date herewith	
U.S. Application Serial No.	filed on	

and, in and to all Letters Patent to be obtained for said invention by the above application or any continuation, division, renewal, substitute, reissue or re-examination thereof.

ASSIGNOR hereby convenants that no assignment, sale agreement or encumbrance has been or will be made or entered into which would conflict with this assignment and sale;

ASSIGNOR further convenants that ASSIGNEE will, upon its request, be provided promptly with all pertinent facts and documents relating to said invention and said Letters Patent and legal equivalents as may be known and accessible to ASSIGNOR and will testify

PATENT REEL: 012406 FRAME: 0982 as to the same in any interference or litigation related thereto and will promptly execute and deliver to ASSIGNEE or its legal representatives any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce said application, said invention and said Letters Patent and said equivalents thereof which may be necessary or desirable to carry out the purposes thereof.

IN WITNESS WHEREOF, I/we have set our hand and seal

November 29, 2001	Kim
Date	INVENTOR: Jong Heon KIM
Date	INVENTOR:

0699P/1

RECORDED: 12/18/2001

PATENT REEL: 012406 FRAME: 0983