

FORM PTO-1619A

Expires 06/30/99
OMB 0651-0027

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Page 2

U.S. Department of Commerce
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PATENT**Correspondent Name and Address****Area Code and Telephone Number** (703) 696-4006**Name** JEAN TRUMAN**Address (line 1)** OFFICE OF NAVAL RESEARCH**Address (line 2)** OFFICE OF COUNSEL**Address (line 3)** 800 NORTH QUINCY STREET**Address (line 4)** ARLINGTON VA 22217-5660**Pages**

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Application Number(s) or Patent Number(s)

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Patent Application Number(s)

6152613

Patent Number(s)If this document is being filed together with a new Patent Application, enter the date the patent application was signed by the first named executing inventor. Month Day Year**Patent Cooperation Treaty (PCT)**

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26852

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PATENT
REEL: 012407 FRAME: 0422

License to the United States Government

Intention Title: Circuit Implementations for Asynchronous Processors

Inventors: Alain Martin and Steven Burns

Patent or Application Serial No: 6,152,613

US Filing/Issue Date: 11/25/1997

Grant/Contract Identification No: N00014-87-K-0745

Foreign Applications filed/intended in (countries):

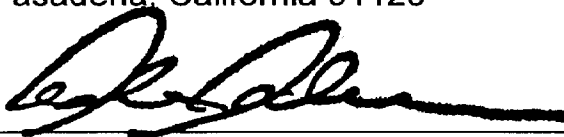
The invention identified above is a Subject Invention under 35 U.S.C. 200, et seq., and the Standard Patent Rights clause at 37 CFR 401.14 or FAR 52.227-11, which are included among the terms of the above-identified grant/contract award from the Office of Naval Research. This document is confirmatory of:

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The Government is here by granted an irrevocable power to inspect and make copies of the above-identified patent application.

California Institute of Technology
1200 E. California Boulevard
Pasadena, California 91125

Dated: February 28, 2001



Adam Cochran
The Intellectual Property Counsel

CIT File No: 2048-1-D