

01-10-2002

FORM PTO-1595

RECO



U.S. DEPARTMENT OF COMMERCE

2001 Staas & Halsey LLP

101939785

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

jc079 U.S. PTO
10/033938
01/03/02

1. Name of conveying party(ies):
Hiroshi SATO

01-03-02

2. Name and Address of receiving party(ies):

FUJITSU LIMITED
1-1, Kamikodanaka, 4-chome
Nakahara-ku, Kawasaki-shi
Kanagawa 211-8588 Japan

3. Nature of conveyance:

Assignment Merger
 Security Agreement Change of Name
 Other: _____

Execution Date(s): December 12, 2001

4. Application number(s) or patent number(s):

This document is being filed together with a new application:

10,033938

(a) The execution date(s) of the application is/are: December 12, 2001

(b) The title is: METHOD FOR LAYOUT DESIGN AND TIMING ADJUSTMENT OF LOGICALLY
DESIGNED INTEGRATED CIRCUIT
OR

This document is being filed after filing of the application:

(a) Patent Application No(s). _____ / _____, filed _____; or

(b) Patent No(s). _____, issued _____

5. Name and address of party to whom correspondence concerning document should be mailed:

STAAS & HALSEY LLP Our Docket: 1093.1047
Attention: James D. Halsey, Jr.
700 Eleventh Street, N.W., Suite 500
Washington, D.C. 20001

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41)..... (\$ 40.00 per Assignment)

Enclosed
 Authorized to be charged to deposit account.

8. Deposit Account No.: 19-3935 (Any underpayment is authorized to be charged to this Deposit Account)
(Attach duplicate copy of this page if paying by deposit account)

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

01/04/2002 HLE333 00000026 10033938

02 FC:581 40.00 0P

James D. Halsey, Jr., Reg. No. 22,729
Name of Person Signing

Signature

December 31, 2001
Date

Total number of pages including cover sheet: 2

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U.S. ASSIGNMENT

5&H 7/84

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter, "ASSIGNOR") by

(Insert Name(s) & Address(es) of ASSIGNEE(S))

FUJITSU LIMITED

1-1, Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8588 Japan

(hereinafter, "ASSIGNEE"), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

(Title of Invention)

METHOD FOR LAYOUT DESIGN AND TIMING ADJUSTMENT OF LOGICALLY DESIGNED

INTEGRATED CIRCUIT

relating to International Patent Application PCT/JP_____/_____ and/or for which application for Letters Patent of the United States was executed on even date herewith or, if not so executed, was:

(a) executed on _____; (Insert date of execution of application, if not concurrent)

(b) filed on _____

Serial No. ____/_____;

Any registered attorney of STAAS & HALSEY, 700 Eleventh Street, N.W., Washington, D.C. 20001 (202/434-1500) is hereby authorized to insert in (b) the specified data, when known.

and to said application and all Letters Patent(s) of the United States granted on said application and any continuation, division, renewal, substitute, reissue or reexamination application based thereon, for the full term or terms for which the said Letters Patent(s) may be granted and including any extensions thereof (collectively, hereinafter, "said application(s) and Letters Patent(s)").

The ASSIGNOR agree(s), when requested by said ASSIGNEE and without charge to but at the expense of said ASSIGNEE, to do all acts which the ASSIGNEE may deem necessary, desirable or expedient, for securing, maintaining and enforcing protection for said invention, including in the preparation and prosecution of said application(s) and the issuance of said Letters Patent(s), in any interference, reissue, reexamination, or public use proceeding, and in any litigation or other legal proceeding which may arise or be declared in relation to same, such acts to include but not be limited to executing all papers, including separate assignments and declarations, taking all rightful oaths, providing sworn testimony, and obtaining and producing evidence.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

(Typed Name & Signature of Inventor(s))

(Date)

(Typed Name & Signature of Witness(es))

1) Hiroshi Sato Dec. 12, 2001 Yasuko Watanabe
Hiroshi Sato Yasuko Watanabe

2) _____

3) _____

4) _____

5) _____

6) _____

PATENT

RECORDED: 01/03/2002

REEL: 012429 FRAME: 0478