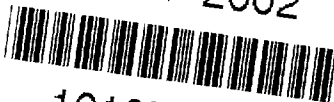


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Commissioner for Patents: Please record the attached original document(s) or copy(ies).

1. Name of conveying party(ies): (a) Brian Doyle; (b) Mark Doczy and (c) Pat Stokley Additional name(s) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		2. Name and address of receiving party(ies): Intel Corporation 2200 Mission College Boulevard Santa Clara, CA 95052 Additional names/addresses attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other: Execution Date: (a) 12/22/01; (b) 01/03/02; (c) 01/03/02		4. Application number(s) or patent number(s): If this document is being filed with a new application, the execution date of the application is: (a) 12/22/01; (b) 01/03/02; (c) 01/03/02 A. Patent Application No(s): B. Patent No(s): Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	
5. Name/address of party to whom correspondence concerning document should be mailed: SCOTT C. HARRIS Fish & Richardson P.C. 4350 La Jolla Village Drive, Suite 500 San Diego, California 92122		6. Total number of applications/patents involved: 1 7. Total fee (37 CFR §3.41): \$40 <input checked="" type="checkbox"/> Enclosed <input type="checkbox"/> Authorized to charge Deposit Account. 8. Deposit Account No.: 06-1050 Please apply any additional charges, or any credits, to our Deposit Account No. 06-1050.	
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9. Statement and Signature: <i>To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.</i>  Scott C. Harris Reg. No. 32,030 Name of Person Signing  Signature  Date: 1/7/02			
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January 9, 2002  
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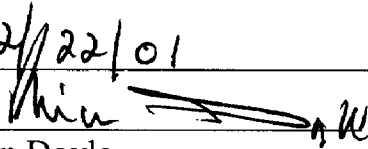
Signature

Gabe Lewis  
Typed Name of Person Signing Certificate

PATENT  
REEL: 012484 FRAME: 0951

ASSIGNMENT

For valuable consideration, we, BRIAN DOYLE of Portland, OR, MARK DOCZY of Beaverton, OR, and PAT STOKLEY of Aloha, OR, hereby assign to Intel Corporation, a Delaware corporation having a place of business at 2200 Mission College Boulevard, Santa Clara, CA 95052; and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us, entitled HARDMASK GATE PATTERNING TECHNIQUE FOR ALL TRANSISTORS USING SPACER GATE APPROACH FOR CRITICAL DIMENSION CONTROL, filed \_\_\_\_\_, and assigned U.S. Serial Number \_\_\_\_\_, and we authorize and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the filing date and serial number of said application when known; this assignment including said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and we authorize the Assignee to apply in all countries in our name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and we agree for ourselves and our respective heirs, legal representatives and assigns, without further compensation to perform such lawful acts and to sign such further applications, assignments, Preliminary Statements and other lawful documents as the Assignee may reasonably request to effectuate fully this assignment.

DATE: 12/22/01  
NAME:   
Brian Doyle

DATE: \_\_\_\_\_  
NAME: \_\_\_\_\_  
Mark Doczy

DATE: \_\_\_\_\_  
NAME: \_\_\_\_\_  
Pat Stokley

ASSIGNMENT

For valuable consideration, we, BRIAN DOYLE of Portland, OR, MARK DOCZY of Beaverton, OR, and PAT STOKLEY of Aloha, OR, hereby assign to Intel Corporation, a Delaware corporation having a place of business at 2200 Mission College Boulevard, Santa Clara, CA 95052; and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us, entitled HARDMASK GATE PATTERNING TECHNIQUE FOR ALL TRANSISTORS USING SPACER GATE APPROACH FOR CRITICAL DIMENSION CONTROL, filed \_\_\_\_\_, and assigned U.S. Serial Number \_\_\_\_\_, and we authorize and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the filing date and serial number of said application when known; this assignment including said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and we authorize the Assignee to apply in all countries in our name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and we agree for ourselves and our respective heirs, legal representatives and assigns, without further compensation to perform such lawful acts and to sign such further applications, assignments, Preliminary Statements and other lawful documents as the Assignee may reasonably request to effectuate fully this assignment.

DATE: \_\_\_\_\_

NAME: \_\_\_\_\_  
Brian Doyle

DATE: 11/3/02

NAME: Mark Doczy  
Mark Doczy

DATE: \_\_\_\_\_

NAME: \_\_\_\_\_  
Pat Stokley

ASSIGNMENT

For valuable consideration, we, BRIAN DOYLE of Portland, OR, MARK DOCZY of Beaverton, OR, and PAT STOKLEY of Aloha, OR, hereby assign to Intel Corporation, a Delaware corporation having a place of business at 2200 Mission College Boulevard, Santa Clara, CA 95052; and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us, entitled HARDMASK GATE PATTERNING TECHNIQUE FOR ALL TRANSISTORS USING SPACER GATE APPROACH FOR CRITICAL DIMENSION CONTROL, filed \_\_\_\_\_, and assigned U.S. Serial Number \_\_\_\_\_, and we authorize and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the filing date and serial number of said application when known; this assignment including said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and we authorize the Assignee to apply in all countries in our name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and we agree for ourselves and our respective heirs, legal representatives and assigns, without further compensation to perform such lawful acts and to sign such further applications, assignments, Preliminary Statements and other lawful documents as the Assignee may reasonably request to effectuate fully this assignment.

DATE: \_\_\_\_\_

NAME: \_\_\_\_\_  
Brian Doyle

DATE: \_\_\_\_\_

NAME: \_\_\_\_\_  
Mark Doczy

DATE: 1-3-02

NAME: Pat Stokley  
Pat Stokley

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