

01-24-2002

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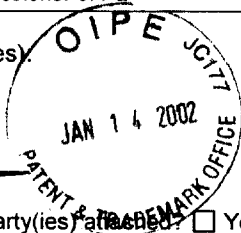
To the Honorable Commissioner of Pa

101957091

original documents or copy thereof.

1. Name of conveying party(ies)

Patra Priyadarsan
Barbara A. Chappell



1-14-02

Additional name of conveying party(ies) attached? ☐ Yes ☒ No

2. Name and address of receiving party(ies)

Name: Intel Corporation

Internal Address: _____

Street Address: 2200 Mission College Blvd.

P.O. Box 58119

City: Santa Clara State: CA Zip: 95052

Additional Name(s) & address(es) attached? ☐ Yes ☒ No

3. Nature of conveyance:

- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Other

Execution Date: December 14, 2001 and December 27, 2001

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution data of the application is:

A. Patent Application No.(s)

09/964,803

B. Patent No.(s)

Additional numbers attached? ☐ Yes ☐ No

5. Name and address of party to whom correspondence concerning this document should be mailed:

Name: B. Delano Jordan Esq.

Internal Address: Kenyon & Kenyon

Street Address: 1500 K Street NW, Suite 700

City: Washington State: DC Zip: 20005

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41) \$ 40.00

- ☐ Enclosed
☒ Authorized to be charged to deposit account

8. Deposit account number:

11-0600

(Attach duplicate copy of this page if paying by deposit account)

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

B. Delano Jordan (43,698)

Name of Person Signing

Signature

January 14, 2002

Date

Total number of pages including cover sheet, attachments, and documents: 3

Mail documents to be recorded with required cover sheet information to:
Commissioner of Patents & Trademarks, Box Assignments
Washington, D.C. 20231

01/23/2002 DBYRNE

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40.00 CH

ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we hereby sell, assign, and transfer to Intel Corporation, a corporation of Delaware, having a principal place of business at 2200 Mission College Blvd., P.O. Box 58119, Santa Clara, California 95052-8119, ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements that are disclosed in the application for the United States patent entitled

TIME SYNTHESIS FOR POWER OPTIMIZATION OF HIGH PERFORMANCE CIRCUITS

and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application;

agree that said Assignee may apply for and receive a patent or patents for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Each Inventor: Please Sign and Date Below:

<u>12/14/</u> , 2001	<u>Priyadarsan Patra</u>	Priyadarsan Patra
_____, 2001	_____	Barbara Chappell
_____, 2001	_____	

Assignment Document Return Address:

Kenyon & Kenyon
1500 K Street, N.W., Suite 700
Washington, D.C. 20005-1257

ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we hereby sell, assign, and transfer to Intel Corporation, a corporation of Delaware, having a principal place of business at 2200 Mission College Blvd., P.O. Box 58119, Santa Clara, California 95052-8119, ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements that are disclosed in the application for the United States patent entitled

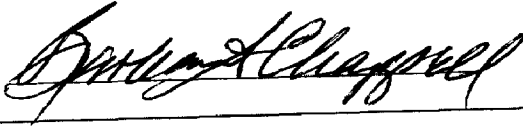
TIME SYNTHESIS FOR POWER OPTIMIZATION OF HIGH PERFORMANCE CIRCUITS

and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application;

agree that said Assignee may apply for and receive a patent or patents for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Each Inventor: Please Sign and Date Below:

_____, 2001	_____	Priyadarsan Patra
27 Dec, 2001		Barbara Chappell
_____, 2001	_____	

Assignment Document Return Address:

Kenyon & Kenyon
1500 K Street, N.W., Suite 700
Washington, D.C. 20005-1257