| FORM PTO-1595 U.S. DEPARTMENT OF COMMERCE | | | |
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| To the | taas & Halsey LLP Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof. | | |
| 1. | Name of conveying party(ies): Teruhiko KAMIGATA Hideo MIYAKE - 24-02 | | |
| 2. | Name and Address of receiving party(ies): | | |
| | FUJITSU LIMITED 1-1, Kamikodanaka, 4-chome Nakahara-ku, Kawasaki-shi Kanagawa 211-8588 Japan | TO PTO | |
| 3. | Nature of conveyance: X Assignment | 11011 U.S. P | |
| 4. | Application number(s) or patent number(s): This document is being filed together with a new application: (a) The execution date(s) of the application is/are: January 11, 2002 (b) The title is:INSTRUCTION PROCESSING METHOD FOR VERIFYING BASIC INSTRUCTION ARRANGEMENT IN VLIW INSTRUCTION FOR VARIABLE LENGTH VLIW PROCESSOR This document is being filed after filing of the application: (a) Patent Application No(s)/, filed; or | ! | |
| 5. | Name and address of party to whom correspondence concerning document should be mailed: STAAS & HALSEY LLP Attention: H. J. Staas 700 Eleventh Street, N.W., Suite 500 Washington, D.C. 20001 Oz FC:581 Our Docket: 1614.1210 01/28/2002 SMINASS1 00000044 10053707 02 FC:581 40.00 GP | | |
| 6. | Total number of applications and patents involved:1 | | |
| 7. | Total fee (37 CFR 3.41)(\$ 40.00 per Assignment) X Enclosed Authorized to be charged to deposit account. | | |
| 8. | Deposit Account No.: 19-3935 (Any underpayment is authorized to be charged to this Deposit Account (Attach duplicate copy of this page if paying by deposit account) | nt) | |
| 9. | Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. | ı | |
| | Staas, Reg. No. 22,010 of Person Signing Signature January 24, 2002 Date | | |

Total number of pages including cover sheet: 2

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| IN CONSIDERATION of the sum of One undersigned inventor(s) (hereinafter, "ASSIGN | Dollar (\$1.00), and of other good and valuable consideration paid to the NOR") by |
|---|---|
| (Insert Name(s) & Address(es) of ASSIGNEE(S | |
| FUJITSU LIMITED | |
| 1-1, Kamikodanaka 4-chor | me, Nakahara-ku, |
| Kawasaki-shi, Kanagawa | 211-8588 Japan |
| | |
| | |
| | |
| | th is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns usive right, title and interest to the invention entitled |
| (Title of Invention) | |
| | METHOD FOR VERIFYING BASIC INSTRUCTION |
| ARRANGEMENT IN VLIW INS | TRUCTION FOR VARIABLE LENGTH VLIW PROCESSO |
| (a) executed on | |
| renewal, substitute, reissue or reexamination app | the United States granted on said application and any continuation, division, plication based thereon, for the full term or terms for which the said Letters tensions thereof (collectively, hereinafter, "said application(s) and Letters |
| to do all acts which the ASSIGNEE may deem a protection for said invention, including in the preparent(s), in any interference, reissue, reexaminate which may arise or be declared in relation to same | said ASSIGNEE and without charge to but at the expense of said ASSIGNEE, necessary, desirable or expedient, for securing, maintaining and enforcing aration and prosecution of said application(s) and the issuance of said Letters tion, or public use proceeding, and in any litigation or other legal proceeding ne, such acts to include but not be limited to executing all papers, including all rightful oaths, providing sworn testimony, and obtaining and producing |
| IN WITNESS WHEREOF, the undersigned inve | antor(s) has (have) affixed his/her/their signature(s). |
| (Typed Name & Signature of Inventor(s)) | (Date) (Typed Name & Signature of Witness(es)) |
| Teruhiko Kamigata | January 11, 2002 Yumiko Odanaga Yumiko Odanaga |
| Teruhiko Kamigata | Yumiko Odanaga |
| Nideo Miyake Hideo Miyake | January 11, 2002 Yuniko Odanaga |
| Hideo Miyake | rumiko Odanaga ° |

RECORDED: 01/24/2002

PATENT REEL: 012524 FRAME: 0527