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OMB No. 0651-0027 (exp. 5/31/2002)

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To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof. Name of conveying party(ies): 2. Name and address of receiving party(ies) Name: SSM Technologies, LLC **Busless Computers SARL** Internal Address: \_\_\_\_\_ Additional name(s) of conveying party(ies) attached? 🖵 Yes 🔀 No 3. Nature of conveyance: Merger | Assignment Street Address: 812-130 S.Winchester Blvd. #118 Security Agreement Change of Name Other City: San Jose State: -CA Execution Date:\_\_3 28 02 4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is:\_\_\_ B. Patent No.(s) A. Patent Application No.(s) 6.345.321 6,112,287 5.598.554 10/022,421 Additional numbers attached? Yes No 6. Total number of applications and patents involved: 4 5. Name and address of party to whom correspondence concerning document should be mailed: 7. Total fee (37 CFR 3.41).....\$\_280.00 Name: Walstein Bennett Smith III Enclosed Internal Address: Authorized to be charged to deposit account 8. Deposit account number: Street Address: P. O. Box 1668 50-0689 (Attach duplicate copy of this page if paying by deposit account) City: Georgetown State: TX Zip: 78627 DO NOT USE THIS SPACE 9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. April 16, 2001 Walstein Bennett Smith III Date Name of Person Signing 00000060 500689 10022421 Total number of pages including cover sheet, attachments, and documents: 3 04/19/2002 DBYRNE

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160.00 CH 120.00 CH Mail documents to be recorded with required cover sheet information to:

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## ASSIGNMENT OF PATENTS AND PATENT APPLICATIONS

WHEREAS, Busless Computers SARL, a corporation organized under the laws of France, (the "Assignor"), is a signatory to the Contribution Agreement, dated March 2002 (the "Agreement") and desires, pursuant to the terms and provisions of the Agreement, to assign to SSM Technologies, LLC, a Delaware limited liability company (the "Assignee"), the entire right, title and interest in the patent(s) and/or application(s) for letters patent identified in Exhibit A, hereto (the "Patents").

NOW, THEREFORE, FOR GOOD AND VALUABLE CONSIDERATION, the receipt of which is hereby acknowledged, and pursuant to the terms and provisions of the Agreement, Assignor does hereby sell, assign, transfer and deliver unto Assignee all of Assignor's right, title and interest in and to the Patents, and any foreign counterparts thereof, and the inventions claimed therein. Assignor further agrees to execute, or cause to be executed, all further documents that may be necessary or desirable to secure unto Assignee any and all of the rights, title and interest herein sold, assigned and transferred or acknowledged to be sold, assigned and transferred.

To comply with the regulations governing the recording of this Assignment with the United States Patent & Trademark Office (the "USPTO"), including without limitation 37 C.F.R. § 3.21, Assignor hereby authorizes and requests that Assignee correct any errors in this Assignment or insert any further identification or other information necessary or desirable to make this Assignment suitable for recording in the USPTO when such identification or information becomes known.

BUSLESS COMPUTERS, SARL

Place: Toulouse, France

Date: March 22, 2002

By:

Printed Name: CLAUDINE DOUGNAC

Title: Liquidatore

PATENT REEL: 012598 FRAME: 0842

## **EXHIBIT A**

## **Patents**

| Patent Number                | Date of Patent | Title   | Jurisdiction  |
|------------------------------|----------------|---|---|
| 6,345,321                    | 2/5/02         | Multiple-mode memory component  | United States   |
| 6,112,287                    | 9/29/00        | SHARED MEMORY MULTIPROCESSOR<br>SYSTEM USING A SET OF SERIAL LINKS AS<br>PROCESSORS-MEMORY SWITCH | United States   |
| 5,598,554                    | 1/28/97        | MULTIPORT SERIES MEMORY COMPONENT   | United States   |
| European Patent<br>0 346 420 | 7/20/94        | PROCEDE D'ECHANGE D'INFORMATION<br>DANS UNSYSTEME MULTIPROCESSEUR                                 | Validated in France, Germany, Great-Britain and the Netherlands |

## **Patent Applications**

| Application<br>Serial Number | Date of Filing | <u>Title</u>  | <u>Jurisdiction</u> |
|------------------------------|----------------|---|---------------------|
| 10/022,421                   | 12/20/01       | Process for Exchanging Information in a Multiprocessor System | United States       |

PATENT
RECORDED: 04/18/2002 REEL: 012598 FRAME: 0843