

03-11-2002



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To the Honorable Commissioner of Patents

102008551

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1. Name of conveying party(ies):

Yukiko UMEMOTO
Toshihiro TANAKA
Hiroyuki TANIKAWA
Yutaka SHINAGAWA

Additional names(s) of conveying party(ies) Yes No

2. Name and address of receiving party(ies):

Name: Hitachi, Ltd.

Address: 6, Kanda Surugadai 4-chome

Chiyoda-ku

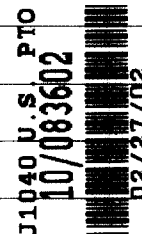
City: Tokyo

State/Prov.:

Country: Japan

ZIP:

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3. Nature of conveyance:

- Assignment Merger
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Execution Date: February 13, 2002

4. Application number(s) or patent numbers(s):

If this document is being filed together with a new application, the execution date of the application is: February 13, 2002

Patent Application No. Filing date

03/05/2002 MGBREM1 00000054 10083602

04 FC:581 40.00 OP

Additional numbers

B. Patent No.(s)

Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Mitchell W. Shapiro

Registration No. 31,568

Address: Miles & Stockbrige P.C.

1751 Pinnacle Drive

Suite 500

City: McLean State/Prov.: VA

Country: US ZIP: 22102-3833

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41):.....\$ 40.00

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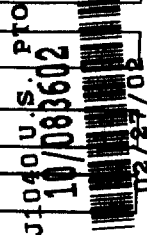
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Name (line 1)	<input type="text" value="Hitachi ULSI Systems Co., Ltd."/>		
Name (line 2)	<input type="text"/>		
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Address (line 2)	<input type="text" value="Kodaira-shi"/>		
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ASSIGNMENT

(譲渡証)

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to the below named inventor(s), citizens of Japan by HITACHI, LTD., and Hitachi ULSI Systems Co., Ltd., corporations organized under the laws of Japan,

located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan, and 22-1, Josuihoncho 5-chome, Kodaira-shi Tokyo, Japan, respectively,

receipt of which is hereby acknowledged, we, the below named inventor(s), do hereby sell and assign to said HITACHI, LTD., and Hitachi ULSI Systems Co., Ltd.,

their successors and assigns, all our right, title and interest, in and for the United States of America, in and to NONVOLATILE SEMICONDUCTOR MEMORY, DATA DELETION METHOD OF NONVOLATILE SEMICONDUCTOR MEMORY, INFORMATION PROCESSING APPARATUS AND NONVOLATILE SEMICONDUCTOR MEMORY SYSTEM invented by us and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD., and Hitachi ULSI Systems Co., Ltd.,

their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by us if this assignment and sale had not been made;

And we hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of said above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And we do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD., and Hitachi ULSI Systems Co., Ltd.,

Signed on the date(s) indicated aside our signatures:

INVENTOR(S)

(発明者フルネームサイン)

Date Signed

(署名日)

1)	<u>Yoshiro Umemoto</u>	<u>2/13/2002 y.u. 2/13/2002 13/2/2002</u>
2)	<u>Toshikiva Tanaka</u>	<u>2/13/2002</u>
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4)	<u>Yutaka Shinagawa</u>	<u>2/13/2002</u>
5)	_____	_____
6)	_____	_____
7)	_____	_____
8)	_____	_____
9)	_____	_____
10)	_____	_____