

04-08-2002



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A. Patent Application No.(s)
B. Patent No.(s)
5,680,344
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CONFIRMATORY INSTRUMENT

Application for: "Circuit And Method Of Operating A Ferroelectric Memory In A DRAM Mode"

Inventor(s): Mirmajid Seyyedy

Patent No: 5,680,344

Contract No: MDA972-94-C-0006

Filing Date: September 11, 1995

Contractor: MICRON Technology, Inc.

The invention identified above is a "Subject Invention" under patent rights clause, included in MDA972-94-C-0006 with the Defense Advanced Research Projects Agency. This document is confirmatory of the paid-up license granted to the Government under this contract in this invention and patent, and of all other rights acquired by the Government by the reference clause.

The Government is hereby granted an irrevocable power to inspect and make copies of the above-identified patent application.

Signed this 30 day of March, 1999

MICRON TECHNOLOGY, INC.

By



D. Mark Durcan,
Chief Technical Officer and Vice President

8000 S. Federal Way/P.O. Box 6, Boise, Idaho 83707-0006

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D. Mark Durcan,
Chief Technical Officer and Vice President

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