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1. Name of conveying party(ies)

Micron Technology, Inc.

3.21.02

2. Name and address of receiving part(ies)

Name: DARPA

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☐ Yes ☒ No

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Street Address:

3701 North Fairfax Drive

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Execution Date:

02/25/02

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Yes



No

FINANCE SECTION

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OFFICE OF PATENT RECORDS

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s)

B. Patent No.(s)

5,665,981

Additional numbers attached?



5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Kimberly Richmond

Internal Address:

Office of General Counsel

Street Address:

3701 North Fairfax Drive

City: ArlingtonState: VAZIP: 22203

6. Total number of applications and patents involved:



7. Total fee (37 CFR 3.41)

\$ 40.00

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40 OF 40

PATENT
REEL: 12745 FRAME: 0740

CONFIRMATORY INSTRUMENT

Application for: "Thin Film Transistors And Method Of Promoting Large Crystal Grain Size In The Formation Of Polycrystalline Silicon Alloy Thin Films"

Inventor(s): Sanjay Banerjee
Shubneesh Batra

Patent No: 5,665,981

Contract No: MDA972-92-C-0054

Filing Date: November 22, 1996

Contractor: MICRON Technology, Inc.

The invention identified above is a "Subject Invention" under patent rights clause, included in MDA972-92-C-0054 with the Defense Advanced Research Projects Agency. This document is confirmatory of the paid-up license granted to the Government under this contract in this invention and patent, and of all other rights acquired by the Government by the reference clause.

The Government is hereby granted an irrevocable power to inspect and make copies of the above-identified patent application.

Signed this 30 day of March, 1999

MICRON TECHNOLOGY, INC.

By



D. Mark Durcan,
Chief Technical Officer and Vice President

8000 S. Federal Way/P.O. Box 6, Boise, Idaho 83707-0006

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