FORM PTO~1595	05-09-20	OO2 R SHEET U.S. DEPT. OF COMMERCE Patent and Trademark	
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CS01-062	1020835	ase record the attached original documents or copy thereof.	U
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1. Name of convey	ying party(ies):	2. Name and address of receiving party(ies):	17971
CHING-THIAM	CHUNG, KAY JIN LEE	Name: Chartered Semiconductor Manufacturing Ltd.	7
Additional name(s) or c	conveying party(ies) attached? Yes X	Internal Address:	
	····	Street Address:	
3. Nature of conveyar X Assignment Security Agree	Merger	60 Woodlands Industrial Park D Street 2 City: Singapore Zip: 738406 Country: Singapore	
other			
Execution Date: _	<u>4/2/02</u>	Additional name(s) & address(es) attached? Yes X	
A A 1' ('	mber(s) or patent number(s):	No	_
	Additional numbers attac	ched? Yes X No	
concerning docu	ess of party to whom correspondence ument should be mailed: DRGE O. SAILE	6. Total no. of applications and patents involved:: 1	
	ss:	7. Total fee (37 CFR 3.41): \$ 40.00	
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Street Address	:_20 MCINTOSH DRIVE	X Authorized to be charged to deposit acct.	
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In consideration of the payment by ASSIGNEE to ASSIGNOR of the sum of One Dollar (\$1.00), the receipt of which is hereby acknowledged, and for other good and valuable consideration, ASSIGNOR: CHING THIAM CHUNG Type or print name of ASSIGNOR Type		By X Inventor(s) or Present Owner				
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Street 2 Singapore 738406 Singapore Nationality and the successors, assigns and legal representatives of the ASSIGNEE (complete one of the following) x the entire right, title and interest an undivided percent (%) interest for the United States and its territorial possessions	ASSIGNEE:					
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x the entire right, title and interest an undivided percent (%) interest for the United States and its territorial possessions	and the successors, assigns and legal repres	sentatives of the ASSIGNEE				
an undivided percent (%) interest for the United States and its territorial possessions	(complete	one of the following)				
	x the entire right, title and interest					
in and to any and all improvements which are disclosed in the invention entitled:		e disclosed in the invention entitled:				

Method Of Wafer Marking For Multi-Layer Metal Processes

PATENT REEL: 012863 FRAME: 0515

and which is found in	
(a) x U.S. patent application executed on even da	ate herewith
(b) U.S. patent application executed on	
(c) U.S. application serial no.	
(d) U.S. patent no.	
(also check (e) if fore.	ign application(s) is also being assigned)
(e) and any legal equivalent thereof in a foreign	country, including the right to clam priority
and, in and to, all Letters Patent to be obtained fo continuation, division, renewal, or substitute, thereof, and thereof.	· · · · · · · · · · · · · · · · · · ·
ASSIGNOR hereby covenants that no assignment be made or entered into which would conflict with this	, sale, agreement or encumbrance has been or will assignment.
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IN WITNESS WHEREOF, H We have signed this	sí an ina)
WARNING Date of signing must be the same as the date of execution	
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If ASSIGNOR is a legal entity complete the following inform	ation
	Type or print the name of the above person authorized to sign on behalf of ASSIGNOR
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261). Use next page if notarization is desired

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