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3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other _____ Execution Date: <u>April 22, 2002</u>	

4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is:	
A. Patent Application No(s).:	B. Patent No(s).: <u>6,051,030 5,551,013, 5,946,472, 6,035,117, 5,622,520, 5,604,665, 5,761,488 and 4,914,612</u>
Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	

5. Name and address of party to whom correspondence concerning document should be mailed: Name: <u>Jeffrey A. Miller</u> Internal Address: LYON & LYON LLP 633 West Fifth Street, Suite 4700 Los Angeles, CA 90071-2066	6. Total number of applications and patents involved: <u>8</u> 7. Total fee (37 CFR 3.41): \$ 320.00 <input checked="" type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Charge this Deposit Account if any additional fee is required 8. Deposit Account Number: <u>12-2475</u>
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9. Statement and signature: To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Jeffrey A. Miller, Reg. No. 35,287 Date: <u>April 25, 2002</u> Total number of pages including cover sheet: <u>3</u>

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ASSIGNMENT OF PATENTS

Whereas, CADENCE DESIGN SYSTEMS, INC., a corporation organized and existing under and by virtue of the laws of the Delaware and having its place of business at 2655 Seely Avenue, San Jose, California 94134 ("CADENCE"), owns of all right, title and interest in the following United States Patents ("the Patents"):

Patent	Date Issued	Inventors	Title
6,051,030	04-18-00	Beausoleil, W.F., et al	Emulation Module Having Planar Array Organization
5,551,013	08-27-96	Beausoleil, W.F., et al	Multiprocessor for Hardware Emulation
5,946,472	08-31-99	Graves, J., et al	Apparatus and Method for Performing Behavioral Modeling in Hardware Emulation and Simulation Environments
6,035,117	03-07-00	Beausoleil, W.F., et al	Tightly Coupled Emulation Processors
5,622,520	04-22-97	Dutka, P., et al	High Density Board Connector Attachment
5,604,665	02-18-97	Chrysler, G., et al.	Multiple Parallel Impingement Flow Cooling with Tuning
5,761,488	06-02-98	Donath, W., et al	Logic Translation Method for Increasing Simulation Emulation Efficiency
4,914,612	04-03-90	Beece, et al	Massively Distributed Simulation Engine

Whereas QUICKTURN DESIGN SYSTEMS, INC., a corporation organized and existing under and by virtue of the laws of the Delaware and having its place of business at 2655 Seely

