

05-16-2002

IN THE UI



ADEMARK OFFICE

Docket No.: NSC-P5183

102091906

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the U.S. Patent and Trademark Office, Assignment Branch, North Tower Building, Suite 10C35, Washington, D.C., 20231, on the below date of deposit.

Date of Deposit:	05/08/02	Name of Person Making the Deposit:	Anthony Chou	Signature of the Person Making the Deposit:	<i>Anthony Chou</i>
------------------	----------	------------------------------------	--------------	---------------------------------------------	---------------------

The Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Please record the attached original document(s) or copy(ies) thereof:

Transmittal of an Assignment



1. Name of conveying party(ies): James T. Doyle and Yushan Li

Serial No.:

Group Art Unit:

Filed: 05/08/02

Examiner:

2. Name and Address of Receiving party(ies): NATIONAL SEMICONDUCTOR CORPORATION
2900 Semiconductor Drive, Santa Clara, CA 95051

3. Nature of Conveyance: Assignment
Execution Date: 04/17/02 and 05/03/02

4. New Patent Application entitled: A CMOS SUB-BANDGAP REFERENCE WITH AN OPERATING SUPPLY VOLTAGE LESS THAN THE BANDGAP POTENTIAL

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: William A. Zarbis
Address: Wagner, Murabito & Hao LLP
Two North Market Street, Third Floor, San Jose, CA 95113
Phone: (408) 938-9060

6. Total Number of applications and patents involved: ONE

7. Fee Calculation (for other than a small entity)

Assignment Recordation Fee, per property

1 X \$40.00

Total Fees (37 CFR 3.41)

\$40.00

X The amount of \$ 40.00 is enclosed

8. At any time during the pendency of this application, please charge any additional fees required or credit any overpayments to Deposit Account 23-0085. A duplicate copy of this transmittal is enclosed.

9. Statement and signature:

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

William A. Zarbis

Name William A. Zarbis
Reg. No. 46,120

Date: 05/08/02

05/14/2002 MBIZUNES 00000017 10142083

02 FC:581

40.00 OP

Assignment to National Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

James T. Doyle

do hereby sell, assign, and transfer unto National Semiconductor Corporation (hereinafter called National Semiconductor), a Delaware Corporation having its principal place of business at 2900 Semiconductor Drive, Santa Clara, CA 95051, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

A CMOS SUB-BANDGAP REFERENCE WITH AN OPERATING SUPPLY VOLTAGE LESS THAN THE BANDGAP POTENTIAL

filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

Serial No.: _____ filed on _____ and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;

And for the above consideration, I/we agree promptly upon request of National Semiconductor, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to National Semiconductor, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by National Semiconductor.

I/we further covenant with National Semiconductor, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature: James T. Doyle Date: 4/17/2002

State of _____
County of _____

Before me this _____ day of _____ personally appeared _____ who is _____ personally known or _____ proved to me on the basis of satisfactory evidence to be the person who acknowledged the foregoing instrument of assignment to be _____ his or _____ her free act and deed.

Notary Stamp/Seal

Notary Public

Assignment to National Semiconductor Corporation

In consideration of good and valuable consideration, receipt of which is hereby acknowledged, I/we

Yushan Li

do hereby sell, assign, and transfer unto National Semiconductor Corporation (hereinafter called National Semiconductor), a Delaware Corporation having its principal place of business at 2900 Semiconductor Drive, Santa Clara, CA 95051, and its successors and assigns, the entire right, title, and interest for the United States and all foreign countries, in and to any and all improvements, including the right of priority in, to, and under, the application for the United States patent entitled:

A CMOS SUB-BANDGAP REFERENCE WITH AN OPERATING SUPPLY VOLTAGE LESS THAN THE BANDGAP POTENTIAL

filed herewith and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor; or

Serial No.: _____ filed on _____ and the inventions set forth and described therein, and any and all Letters Patent of the United States and of countries foreign thereto which may be granted thereon or therefor;

And for the above consideration, I/we agree promptly upon request of National Semiconductor, its successors or assigns, to execute and deliver without further compensation any power of attorney, assignment, application, whether original, continuation, divisional or reissue, or other papers which may be necessary or desirable fully to secure to National Semiconductor, its successors and assigns, the inventions described in said application and all patent rights therein, in the United States and in any country foreign thereto, and to cooperate and assist in the prosecution of interference proceedings involving said inventions and in the adjudication or reexamination of said Letters Patent provided the expenses which may be incurred by me/us in lending such cooperation and assistance are paid by National Semiconductor.

I/we further covenant with National Semiconductor, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned;

In witness whereof, I/we hereunto set my/our hand(s) and seal:

Inventor's Signature: Yushan Li Date: 5/3/2002

State of _____
County of _____

Before me this _____ day of _____ personally appeared _____ who is _____ personally known or _____ proved to me on the basis of satisfactory evidence to be the person who acknowledged the foregoing instrument of assignment to be _____ his or _____ her free act and deed.

Notary Stamp/Seal

Notary Public