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HITA.0194 To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof. Name and Address of Receiving Party(ies): 1. Name of Conveying Party(ies): Ichiro KOHNO Hitachi, Ltd. Name: Address: 6, Kanda Surugadai 4-chome Additional names of conveying parties attached Chiyoda-ku, Tokyo, Japan City: Nature of Conveyance: Merger Assignment Change of Name Security Agreement Other: Execution Date: April 26, 2002 (a) Patent Application Number(s): (b) Patent Numbers: If this document is being filed together with a new application, the execution date of the application is: April 26, 2002 Additional Numbers Attached. Total Number of Applications and Patents Involved: 1 5. Name and Address of Party to whom Correspondence Concerning this Document Should be Mailed: Stanley P. Fisher Name: \$40.00 Total Fee: (37 C.F.R. § 3.41) Address: Reed Smith LLP X Enclosed. 3100 Fairview Park Dr. Authorized to be charged to deposit account. \boxtimes Suite 1400 Falls Church, Va. 22042 Deposit Account Number: 08-1480 ATTACH DUPLICATE COPY OF THIS PAGE IF PAYING BY DEPOSIT ACCOUNT DO NOT USE THIS SPACE Statement and Signature: To the best of my knowledge and belief, the foregoing is true and correct and any attached copy is a true copy of the original document. May 14, 2002 Registration No. 24 JUAN CARLOS A. MARQUEZ Registration No. 34,072 Notal number of pages comprising cover sheet: 1

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PATENT REEL: 012898 FRAME: 0572

ASSIGNMENT

(譲渡証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by HITACHI, LTD., a corporation organized under the laws of Japan, located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan, receipt of which is hereby acknowledged I do hereby sell and assign to said HITACHI, LTD., its successors and assigns, all my right, title and interest, in and for the United States of America, in and to

A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND FAULT-DETECTING METHOD OF A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD., its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD.

Signed on the date(s) indicated aside our signatures:

RECORDED: 05/14/2002

INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
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2)	
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