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HEET

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Tab settings

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

Gary Q. Jin

5-17-02

Additional names(s) of conveying party(ies) Yes No

2. Name and address of receiving party(ies):

Name: Zarlink Semiconductor Inc.

Address: 400 March Road

MAY 17 2002

3. Nature of conveyance:

- Assignment Merger
- Security Agreement Change of Name
- Other

City: KANATA State/Prov.: ON

Country: CANADA ZIP: K2K 3H4

Execution Date: April 1, 2002

Additional name(s) & address(es) Yes No

4. Application number(s) or patent numbers(s):

If this document is being filed together with a new application, the execution date of the application is: _____

Patent Application No.	Filing date
<u>10/098,474</u>	<u>March 18, 2002</u>

B. Patent No.(s)

Additional numbers Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Richard J. Mitchell

Registration No. 34,519

Address: MARKS & CLERK

P.O. Box 957, Station B

City: Ottawa State/Prov.: ON

Country: CANADA ZIP: K1P 5S7

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41):.....\$ 40.00

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To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Richard J. Mitchell

May 14, 2002

Name of Person Signing

Signature

5

Date

05/22/2002 00000095 10098474 Total number of pages including cover sheet, attachments, and

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40.00

Mail documents to be recorded with required cover sheet information to:
Commissioner of Patents & Trademarks, Box Assignments
Washington, D.C. 20231

PATENT
REEL: 012910 FRAME: 0347

ASSIGNMENT

WHEREAS, **Gary Q. Jin, residing at 51 Liston Cres., Kanata, Ontario, K2L 2W3, Canada,** have invented improvements in **FLEXIBLE BIT SELECTION USING TURBO TRELLIS-CODED MODULATION**, for which an application for United States Letters Patent has been executed by us on even date hereon

WHEREAS, **ZARLINK SEMICONDUCTOR INC.**, whose full post office address is 400 March Road, Kanata, ON, K2K 3H4, Canada, as assignee, is desirous of acquiring all right, title and interest in and to said invention and any Letters Patent that may be granted therefor.

NOW, THEREFORE, in consideration of the sum of One Dollar (\$1.00) and other good and valuable consideration, the receipt of which is hereby acknowledged, I, as assignor hereby sell, assign and set over to said assignee the entire right, title and interest for the United States of America and all other countries, in and to said invention and the aforesaid application for Letters Patent, all original, divisional, continuation, substitute or reissue applications and patents applied for or granted therefor in the United States of America and all other countries, and the Commissioner of Patents and Trademarks is hereby authorized and requested to issue all patents on said improvements or resulting therefrom to said assignee herein, as assignee of the entire interest therein; and the undersigned for themselves and their legal representatives, heirs and assigns do hereby agree and covenant without further remuneration, to execute and deliver all divisional, continuation, reissue and other applications for Letters Patent on said improvements and all assignments thereof to said assignee or its assigns, to communicate to said assignee or its representatives all facts known to the undersigned respecting said improvements, whenever requested, to testify in any interference or other legal proceedings in which any of said applications or patents may become involved, to sign all lawful papers, make all rightful oaths, and to do generally everything necessary to aid assignee, its successors, assigns and nominees to obtain patent protection for said improvements in all countries, the expenses incident to said applications to be borne and paid for by said assignee.

Signed at Kanata this 1 day of April 2002

J. Q. Jin

Gary Q. Jin

M. Helman

(Witness)