5/15/02

ASSIGNM ||||||||||||||||||||||||| **HEET**

102103451

To: Honorable Commissioner of Patents and Trademarks:

Please record the attached original document or copy thereof.

1. Name of conveying parties:
    a) Meng-chi Hsu
    b) Wei-ren Lo

2. Name and address of receiving party:
    a) Name: ADMtek Incorporated
        Address: 1F, No. 9, Industry E. 9th Rd., Science-based Industrial Park,
                  Hsinchu, Taiwan, R.O.C.

3. Nature of conveyance
    ☒ Assignment   ☐ Merger
    ☐ Security Agreement   ☐ Change of Name
    ☐ Other _____   ☐ License Agreement

    Execution Dates: April 29, 2002, respectively.

4. Application Number(s) or Patent Number(s): Not yet assigned *10/150252*

    The title of the new application is: ETHERNET SWITCHING ARCHITECTURE
    AND DYNAMIC MEMORY ALLOCATION METHOD FOR THE SAME

5. Please send all correspondence concerning this document to:

    05/22/2002 GGEDREGI 00000051 10150252

    03 FC:581   40.00 OP

    Albert S. Penilla
    MARTINE & PENILLA, LLP
    710 Lakeway Drive, Suite 170
    Sunnyvale, CA 94085
    Customer Number 25920
    Tel. No. (408) 749-6900
    Fax No.: (408) 749-6901
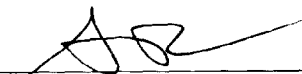
6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41): $40.00

    ☒ Enclosed
    ☐ Authorized to be charged to Deposit Account No. 50-0805
       (Order No. JLINP099)

8. To the best of my knowledge and belief, the foregoing information is true and correct
    and any attached copy is a true copy of the original document.

Date: May 15, 2002

_____
Albert S. Penilla, Esq.
Registration No. 39,487

Attorney Docket No. JLINP099   (Revised 01/96)

**PATENT
REEL: 012918 FRAME: 0255**

ASSIGNMENT OF PATENT APPLICATION

Q0957

For good and valuable consideration, receipt of which hereby acknowledged,

I/We,

(a) **Meng-chi HSU**
(b) **Wei-ren LO**

residing at

(a) **1F, No. 9, Industry E. 9th Rd., Science-based Industrial Park, Hsinchu, Taiwan, R.O.C.**
(b) **1F, No. 9, Industry E. 9th Rd., Science-based Industrial Park, Hsinchu, Taiwan, R.O.C.**

assign and transfer to: **ADMtek Incorporated**

a corporation organized under the law of Taiwan and having a place of business at :
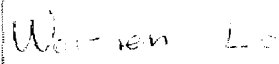
1F, No. 9, Industry E. 9th Rd., Science-based Industrial Park, Hsinchu, Taiwan, R.O.C.

its successors and assigns, my entire right, title and interest, including the right of priority, in, to and under an application for Letters Patent of the United States entitled:

ETHERNET SWITCHING ARCHITECTURE AND DYNAMIC MEMORY ALLOCATION METHOD FOR THE SAME

including divisions, reissues, continuations and extensions thereof, and all rights of priority without further authorization from me; I agree to execute all papers useful in connection with said United States application, and generally to do everything possible to aid said assignee, its successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in United States; and I request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Joint Inventor (a)	**Meng-chi HSU**		
Inventor's Signature		Date	7.002 · 4 · 29
Residence	Hsinchu	Citizenship	Taiwan, R.O.C.

Joint Inventor (b)	**Wei-ren LO**		
Inventor's Signature		Date	2002 · 4 · 29
Residence	Hsinchu	Citizenship	Taiwan, R.O.C.