RECORDATION FORM COVER SHEET EPARTMENT OF COMMERCE Form PTO-1595 102109071 PAIENIS ONLY Patent and Trademark Office Attorney Docket No. 02-134 To the Commissioner for Patents: Please record the attached original documents or copy thereof. 1. Name of conveying party(ies): 2. Name and address of receiving party(ies): Viswanathan LAKSHMANAN Michael JOSEPHIDES Tom R. O'BRIEN LSI Logic Corporation 1551 McCarthy Boulevard David A. MORGAN Milpitas, CA 95035 Additional name(s) of conveying party(ies) attached? [ ] Yes [X ] No 3. Nature of conveyance: [] Merger [X] Assignment [ ] Security Agreement [ ] Change of Name [ ] Other \_\_\_ Execution Date: May 17, 2002 4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is: May 17, 2002 B. Patent No.(s) A. Patent Application No.(s) Additional numbers attached? [ ] Yes [X] No 6. Total number of applications and patents involved: 1 5. Name and address of party to whom correspondence concerning document should be mailed: 7. Total fee (37 CFR 3.41): \$40.00 LSI LOGIC CORPORATION 1551 McCarthy Blvd. [ ] Enclosed Milpitas, CA 95035 [x] Authorized to be charged to deposit account should the fee (408) 433-8708 not be enclosed. 10151826 8. Deposit account number: 12-2252 00000053 122252 05/31/2000 DBYRNE 40.00 CH DO NOT USE THIS SPACE 9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. May 22, 2002 Charles W. Peterson, Jr. Date Registration No. 34,406 Signature 10. Total number of pages including cover sheet, attachments and document: 2

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**PATENT** REEL: 012935 FRAME: 0023 .Docket No.: 02-134 72612 (6653)

Patent

## ASSIGNMENT

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/WE, the undersigned, Viswanathan Lakshmanan, Michael Josephides, Tom R. O'Brien and David A. Morgan do hereby sell, assign, and transfer to: LSI LOGIC CORPORATION, a corporation of Delaware, having a place of business at 1551 McCarthy Boulevard, Milpitas, California 95035, ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all inventions and improvements which are disclosed in the application for United States Letters Patent,

and is entitled: <u>CHIP DESIGN METHOD FOR DESIGNING INTEGRATED CIRCUIT CHIPS WITH EMBEDDED MEMORIES</u> and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said

(X) which has been executed by the undersigned concurrently herewith,

RECORDED: 05/22/2002

( ) which was filed on \_\_\_\_\_\_, 20\_\_ and assigned Serial Number \_\_\_\_\_.

improvements; and in and to all rights of priority resulting from the filing of said United States application; and

Agree that said Assignee may apply for and receive Lewithout charge to, but at the expense of, said Assignet the intent and purpose of this Assignment, the undersiall other patent applications on any and all said improvements; communicate to said Assignee, its successors said improvements and the history thereof; and gene representatives shall consider desirable for aiding in so for vesting title to said improvements and all applications successors, assigns and legal representatives; and	e, its successors, assigns an gned will execute all division vements; execute all rightful assigns, and representative rally do everything possible ecuring and maintaining pro-	d legal representatives, to carry out in good faith onal, continuing, substitute, renewal, reissue, and oaths, assignments, powers of attorney and other es, all facts known to the undersigned relating to which said Assignee, its successors, assigns or oper patent protection for said improvements and
Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.		
Name: Viswanathan Lakshmanan 600 W. 123 <sup>rd</sup> Avenue, #3614 Westminster, OR 80234	Date: 05/17/2002	Witness: Vance Threatt Printed
Name: Michael Josephides 730 Copper Lane, #202 Louisville, CO 80027	Date: 05/17/2002	Witness: Vance Threatt Printed
Name: Tom R. O'Brien 3400 Sam Houston Circle Ft. Collins, CO 80526	Date: <u>5/17/200</u> 2	Witness: Vauce Threatt Printed
Name: David A. Morgan 6001 Huntington Hills Court Et. Collins, CO 80525	Date: 5/17/07	Witness: Vance Threatt Printed

PATENT REEL: 012935 FRAME: 0024