To the Honorable Comm.
Please record the attack

06-21-2002



arks:
:hereof.

102130854

1. Name of Party(ies) conveying an interest:

Tatsumi Yamauchi Kazuharu Kuchimachi

JUN 1 7 2002

2. Name and Address of Party(ies) receiving an interest:

HITACHI, LTD. 6, Kanda Surugadai 4-chome Chiyoda-ku, Tokyo, JAPAN

3. Description of the interest eyed:

Assignment

4. Application number(s) or patent number(s).

Additional sheet attached? Yes XXX No

6.17.02

If this document is being filed together with a new application, the execution date of the application is:

A. Patent Application No.(s)

B. Patent No.(s)

09/381,170

5. Name and address of party to whom correspondence concerning document should be mailed:

> John R. Mattingly Mattingly, Stanger & Malur, P.C. 1800 Diagonal Road, Suite 370 Alexandria, Virginia 22314

- 6. Number of applications and patents involved: One
- 7. Amount of fee enclosed or authorized to be charged: \$40.00
- The Commissioner is hereby authorized to charge Deposit Account No. 50-1417 if no check is attached.

DO NOT USE THIS SPACE

- 9. Execution date of attached document: September 1, 1999
- 10. To the best of my knowledge and belief, I declare under penalty of perjury under the laws of the United States of America that the foregoing information is true and correct and that any attached copy is a true copy of the original document.

John R. Mattingly Name of Person Signing

ionature

June 17, 2002

Date

Total number of pages being submitted:

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PATENT REEL: 012998 FRAME: 0604

ASSIGNMENT (譲 渡 証)

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to me citizen of Japan by HITACHI, LTD., a corporation organized under the laws of Japan, located at 6, Kanda Surugadai 4-chome, Chiyoda-ku, Tokyo, Japan, receipt of which is hereby acknowledged I do hereby sell and assign to said HITACHI, LTD., its successors and assigns, all my right, title and interest, in and for the United States of America, in and to SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

invented by me (if only one is named below) or us (if plural inventors are named below) and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD., its successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by me or us if this assignment and sale had not been made;

And I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of the above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD.

Signed on the date(s) indicated aside signatures:

RECORDED: 06/17/2002

INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)	Witnesses (立会人サイン)
1) Tatsumi Yamanchi	September 1, 1999	Jung Kitanung
2) Kuchimachi Kazukaru	September 1, 1999	Jung Kitanung Jung Kitanung
3)		
4)		
5)		
6)		
7)		
8)		
9)		
10)		

PATENT REEL: 012998 FRAME: 0605