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## License to the United States Government

Invention Title: METHOD OF FORMING A LOW-DEFECT EXPITAXIAL LAYER IN THE FABRICATION OF SEMICONDUCTOR DEVICES

Inventor(s): R. BRUCE DOAK JOHN L EDWARDS, JR. DAVID J SMITH VICTOR M TORRES IGNATIUS S TSONG

Patent or Application Serial No.: 09/414,953

U.S. Filing/Issue Date: 10/08/99

Grant/Contract Identification Number(s): DMR96326

Foreign Applications filed/intended in (countries):

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