FORM PTO-1595 (Modified) (Rev. 03-01) OMB No. 0651-0027 (exp.5/31/2002) P08/REV03 09-04-2002

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Docket No.: SKYWKS.0002P

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U.S. DEPARTMENT OF COMMERCE

Patent and Trademark Office

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102211089 Tab settings → → To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof. 1. Name of conveying party(ies): 2. Name and address of receiving party(ies): Conexant Systems, Inc. 8:27.02 Name: Washington Sub, Inc. Internal Address: Additional names(s) of conveying party(ies) ☐ Yes 🛛 No 3. Nature of conveyance: Street Address: 4311 Jamboree Road ■ Assignment Merger ☐ Change of Name ☐ Security Agreement City: Newport Beach State: CA ZIP: 92660 Other _ Execution Date: June 25, 2002 Application number(s) or patent numbers(s): If this document is being filed together with a new application, the execution date of the application is: B. Patent No.(s) A. Patent Application No.(s) 09/712,771 ☐ Yes ☒ No Additional numbers attached? 5. Name and address of party to whom correspondence 6. Total number of applications and patents involved: concerning document should be mailed: Name: Chad W. Miller 7. Total fee (37 CFR 3.41):....\$ 40.00 Internal Address: Enclosed - Any excess or insufficiency should be credited or debited to deposit account ☐ Authorized to be charged to deposit account 8. Deposit account number: Street Address: Weide & Miller, Ltd. 윱 S 502200 330 South 3rd Street, Suite 1130 (Attach duplicate copy of this page if paying by deposit account State: NV ZIP: 89101 City: Las Vegas DO NOT USE THIS SPACE To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true sopy of the original document. August 20, 2002 Chad W. Miller Date Name of Person Signing

ASSIGNMENT OF INVENTIONS, PATENTS, PATENT APPLICATIONS AND TRADEMARKS

WHEREAS, CONEXANT SYSTEMS, INC. ("Assignor"), a Delaware corporation with an office at 4311 Jamboree Road, Newport Beach, California 92660-3095, owns or has rights to certain inventions, patents, patent applications and trademarks identified in the attached Schedule entitled "IP SCHEDULE A";

WHEREAS, WASHINGTON SUB, INC. ("Assignee"), a Delaware corporation with an office at 4311 Jamboree Road, Newport Beach, California 92660-3095, wishes to acquire full rights and ownership of said inventions, patents, patent applications and trademarks.

NOW THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, Assignor grants, conveys, assigns and transfers to the Assignee and the Assignee's successors and assigns, Assignee's entire right, title and interest in and to said inventions, patents, patent applications and trademarks listed on the attached Schedule identified as "IP SCHEDULE A", including all corresponding applications such as continuations, continuations-in-part, divisionals, provisionals, reissues, reexaminations, and foreign counterparts thereof, along with the subject matter of any and all claims which may be obtained in the aforementioned, in the United States and every foreign country, including all rights to profits and damages by reason of past infringement by any party or parties, with the right to sue and collect same for Assignee's, and Assignee's successors and assigns own use and benefit.

UPON SAID CONSIDERATION, Assignor appoints Assignee and Assignee's successors and assigns as its attorney-in-fact to act in Assignor's name and place to execute, deliver and record any document or instrument of assignment or conveyance necessary to perfect, grant, and confirm the rights granted herein, and Assignor conveys to the Assignee the right to make application, prosecute, receive and enforce in its own behalf and name the inventions, patents, patent applications and trademarks of "IP SCHEDULE A" in the United States and all foreign countries and to claim priority therefrom under the Patent Cooperation Treaty, the International Convention and/or any other international arrangement.

IN WITNESS WHEREOF, Assignor has caused this Assignment to be duly executed by one of its officers on the date shown below.

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CONEXANT SYSTEMS, INC.	WASHINGTON-CUB, INC.
Dennis O'Reilly Name	Balakrishdan S. Iyer Name
General Counsel & Secretary Title	Title
Date: June 25, 2002	

PATENT

REEL: 013241 FRAME: 0631

IP SCHEDULE A (6/7/02)

ACTIVE US PATENTS/APPLICATIONS & DISCLOSURES

Patents
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	22-Nov-1988	01-Apr-1997	12-Jun-1984	03-Sep-1985	27-Oct-1987	09-Apr-1991	23_Jun-1992	03-Oct-1995	25-Feb-1997	17-Oct-1995	04-Jun-1996	01-Dec-1998	02~Jun-1998	03-Mar-1998	19-May-1998
	4,787,058	5,617,063	4,454,481	4,539,536	4,703,205	5,008,824	5,124,578	5,455,542	5,606,732	5,459,762	5,523,719	5,845,204	5,761,250	5,724,657	5,754,601
	13-Sep-19854,787,058	13-Dec-1995 5,617,063	04-Mar-19824,454,481	12-Nov-19824,539,536	26-Jul-1985 4,703,205	07-Apr-1986 5,006,824	01-Oct-19905,124,578	22-Nov-1993 5,455,542	26-Apr-1994 5,606,732	16-Sep-1994	15-Feb-1994 5,523,719	11-Aug-19955,845,204	15-Aug-1995 5,761,250	29-Sep-1995 5,724,657	10-May-1996 5,754,601
	776.181	571,596	354,840	441,195	759,193	849,093	591,164	08/155,500	233,289	307,202	196,328	08/514,509	515,403	08/537,378	844,762
	Granted	Granted	Granted	Granted	Granted	Granted	Granted	Granted	Granted	Granted	Granted	Granted	Granted	Granted	Granted
		ocessing Related		Parameter-Setting Approach t/Obtain HiPerformance CTD Transversal Filters	Uncompensated and Compensated GaAs Input Receivers	Third Mode Torsional E-M Resonator	Receiver Designed with Large Output Drive	Symmetrical Clock Crystal Oscillator Circuit	Direct Connect Radio and Antenna Assembly	Variable Multi-Threshold Detection for 0.3-GMSK	Component Insensitive, Analog Bandpass Filter	Method and Apparatus for Controlling the Wakeup Logic of a Radio Receiver in Steep	An therative Filtering Frequency Estimator	An Adaptive Frequency Correction Burst Detector for the GSM Handset System	
Significant Control of the Control o		00CXT0174W	707404	82E035	85E018	RAEDOR	90E052	COECOSO	82E030	93E067	94E005	94E059	055000	95E003	95E058
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IP SCHEDULE A

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PATENT REEL: 013241 FRAME: 0632

99RSS301	DMA Channel for High-Speed Asynchronous Data Transfer	Pending	09/550,852	17-Apr-2000	
99RSS305	Power Amplifier With Integrated Quarter Wave ner/Combiner Circuit	Pending	09/761,100	16-Jan-2001	
99RSS310	ducing or Eliminating the al Derived from the Output	Pending	09/515,633	29-Feb-2000	
99RSS313	Jser Selectable Information in	Pending	09/842,601	26-Apr-2001	
99RSS316	aturation Detection and Compensation	Pending	09/651,801	30-Aug-2000	
99RSS358	terference Cancellation in GSM	Pending	09/634,081	08-Aug-2000	
99RSS359	etector With Echo Cancellation	Pending	09/792,816	23-Feb-2001	
99RSS386	Power Level Amplifier		09/686,440	10-Oct-2000	
99RSS397	ircuits with Thermal Stability		09/871,517	31-May-2001	
99RSS404	il Wireless Network to Control a the Network	Pending	09/658,729	11-Sep-2000	
99RSS405	45 Degree Polyphase Filter With	Pending	09/666,501	18-Sep-2000	
99RSS406	hase Calibration	Pending	09/823,299	30-Mar-2001	
99RSS410	fixer	Pending	09/668,879	25-Sep-2000	
99RSS422	Module	Pending	09/712,771	13-Nov-2000	
99RSS426	Trap and Patch System for Virtual Replacement of Defective Volatile Memory Cells	Pending	09/671,401	27-Sep-2000	
99RSS429	aptation System for	Pending	09/718,985	22-Nov-2000	
99RSS461	Je Silent Ringing System for Communication	Pending	09/650,543	30-Aug-2000	
99RSS468	I Apparatus for a Direct Conversion Receiver nitter	Pending	09/621,407	21-Jul-2000	

RECORDED: 08/27/2002

P SCHEDULE A

REEL: 013241 FRAME: 0633