

A/D 581-40

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09-06-2002



To the Honorable Com

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1. Name of conveying party(ies):

- 1) Laurence BOISSONNET
- 2) Dominique GOLANSKI
- 3) Bruno RAUBER
- 4) Andre GRANIER

6/27/02

10/169237  
528 Rec'd PCT/PTC 27 JUN 2002 PATENT  
DUCKET NO: 01-GR1-148

VER SHEET - PATENTS

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STMicroelectronics S.A.  
29 boulevard Romain Rolland  
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Additional name(s) & address(es) attached?  
\_\_\_\_ Yes      XXX No

3. Nature of conveyance: XX Assignment Execution Date: 1-4) June 19, 2002

4. Application or patent number(s): 10/169237

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A. Patent Application No.(s):

B. Patent No.(s):

5. Name and address of party to whom correspondence concerning document should be mailed:

Customer Number 23334  
Jon A. Gibbons  
Fleit, Kain, Gibbons, Gutman & Bongini P.L.  
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551 Northwest 77th Street, Suite 111  
Boca Raton, Florida 33487

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41): \$ 40.00

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## ASSIGNMENT

name and  
address of  
assignee

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**STMicroelectronics S.A.**  
**29 boulevard Romain Rolland**  
**F-92120 Montrouge, France**

(hereinafter ASSIGNEE) all right, title and interest for the United States, its territories and possessions in and to this invention relating to

title of  
invention

**METHOD OF SIMULTANEOUSLY FABRICATING A PAIR OF  
INSULATED GATE TRANSISTORS RESPECTIVELY HAVING A  
THIN OXIDE AND A THICK OXIDE, AND AN INTEGRATED CIRCUIT  
INCLUDING A PAIR OF TRANSISTORS OF THIS KIND**

as set forth in this United States Patent Application

check one



executed concurrently herewith



executed on \_\_\_\_\_



Serial No. \_\_\_\_\_ Filed \_\_\_\_\_

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NAME AND SIGNATURE OF INVENTOR(S)

Name: Laurence BOISSONNET	Signature: <u>Laurence BOISSONNET</u>	Date: <u>June 19, 2002</u>
Name: Dominique GOLANSKI	Signature: <u>Dominique Golanski</u>	Date: <u>June 19, 2002</u>
Name: Bruno RAUBER	Signature: <u>Bruno RAUBER</u>	Date: <u>June 19, 2002</u>
Name: Andre GRANIER	Signature: <u>Andre GRANIER</u>	Date: <u>June 19, 2002</u>

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