

09-16-2002

FORM PTO-1595

REC

U.S. DEPARTMENT OF COMMERCE



102222644

2001 Staas & Halsey LLP

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
Yasushi ARAKI

09/10/02

2. Name and Address of receiving party(ies):

SHINKO ELECTRIC INDUSTRIES CO., LTD.
711, Aza Shariden, Oaza Kurita
Nagano-shi Nagano 380-0921 Japan

10/23/02
10/23/02
09/10/02

3. Nature of conveyance:

Assignment ___ Merger
___ Security Agreement ___ Change of Name
___ Other: _____

Execution Date(s): September 2, 2002

4. Application number(s) or patent number(s):

101237797

This document is being filed together with a new application:
(a) The execution date(s) of the application is/are: September 2, 2002

(b) The title is: MULTILAYER INTERCONNECTOR BOARD AND MULTILAYER SEMICONDUCTOR DEVICE

OR

This document is being filed after filing of the application:

(a) Patent Application No(s). ___ / ___, filed _____; or
(b) Patent No(s). _____, issued _____

5. Name and address of party to whom correspondence concerning document should be mailed:

STAAS & HALSEY LLP Our Docket: 300.1101
Attention: H. J. Staas
700 Eleventh Street, N.W., Suite 500
Washington, D.C. 20001

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41)..... (\$ 40.00 per Assignment)

Enclosed
___ Authorized to be charged to deposit account.

8. Deposit Account No.: 19-3935 (Any underpayment is authorized to be charged to this Deposit Account)
(Attach duplicate copy of this page if paying by deposit account)

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

H. J. Staas, Reg. No. 22,010
Name of Person Signing

Signature

September 10, 2002
Date

Total number of pages including cover sheet: 2

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U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter "ASSIGNOR") by

(Insert
ASSIGNEE's
Name(s)
Address(es))

SHINKO ELECTRIC INDUSTRIES CO., LTD.

711, Aza Shariden, Oaza Kurita, Nagano-shi, Nagano 380-0921, Japan

(hereinafter "ASSIGNEE"), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

(Title of
Invention)

MULTILAYER INTERCONNECT BOARD AND MULTILAYER

SEMICONDUCTOR DEVICE

relating to International Patent Application PCT/JP _____/_____ and/or for which application for Letters Patent of the United States was executed on even date herewith or, if not so executed, was:

(Insert date
of execution
of application,
if not
concurrent)

(a) executed on _____ ;

(b) filed on _____,
Serial No. _____/_____ ;

(_____) _____) is hereby authorized to insert in (b) the specified data, when known.

and to said application and all Letters Patent(s) of the United States granted on said application and any continuation, division, renewal, substitute, reissue or reexamination application based thereon, for the full term or terms for which the said Letters Patent(s) may be granted and including any extensions thereof (collectively, hereinafter, "said application(s) and Letters Patent(s)").

The ASSIGNOR agree(s), when requested by said ASSIGNEE and without charge to but at the expense of said ASSIGNEE, to do all acts which the ASSIGNEE may deem necessary, desirable or expedient, for securing, maintaining and enforcing protection for said invention, including in the preparation and prosecution of said application(s) and the issuance of said Letters Patent(s), in any interference, reissue, reexamination, or public use proceeding, and in any litigation or other legal proceeding which may arise or be declared in relation to same, such acts to include but not be limited to executing all papers, including separate assignments and declarations, taking all rightful oaths, providing sworn testimony, and obtaining and producing evidence.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

(Signatures)

1) <u>荒木 康</u> (SIGNATURE)	<u>Yasushi Araki</u> (TYPE NAME)	<u>September 2, 2002</u> (DATE)
2) _____ (SIGNATURE)	_____ (TYPE NAME)	_____ (DATE)
3) _____ (SIGNATURE)	_____ (TYPE NAME)	_____ (DATE)
4) _____ (SIGNATURE)	_____ (TYPE NAME)	_____ (DATE)
5) _____ (SIGNATURE)	_____ (TYPE NAME)	_____ (DATE)