

U.S. DEPARTMENT OF COMMERCE

RECORDATION FORM COVER SHEET
PATENTS ONLYPatent and Trademark Office
Docket No. 26745000044

To the Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

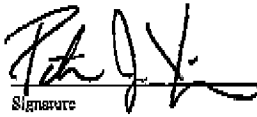
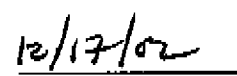
1. Name of conveying party(ies): DSP Semiconductors Ltd. aka DSP Group, Ltd. Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	2. Name and address of receiving party(ies): Name: Corage, Ltd. Street Address: 5 Shenkar Street City: Herzelia Pituach Country: ISRABL ZIP: 46120 Additional name(s) & address(es) attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No
3. Nature of conveyance: <input checked="" type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Other: Execution Date: December 8, 2002	

4. Application number(s) or patent number(s): If this document is being filed together with a new application, the execution date of the application is: A. Patent Application No.(s) <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No B. Patent No.(s) U.S. 5,537,576 & U.S. 5,463,749 Additional numbers attached? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No
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5. Name and address of party to whom correspondence concerning document should be mailed: Peter J. Yim Morrison & Foerster LLP 425 Market Street San Francisco, California 94105-2482	6. Total number of applications and patents involved: 2 7. Total fee (37 C.F.R. § 3.41): \$40.00 <input type="checkbox"/> Enclosed <input checked="" type="checkbox"/> Authorized to be charged to deposit account, referencing Attorney Docket 26745000044 8. Deposit account number: <u>03-1952</u>
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The Commissioner is hereby authorized to charge any fees under 37 C.F.R. § 1.21 that may be required by this paper, or to credit any overpayment to Deposit Account No. 03-1952.

DO NOT USE THIS SPACE

9. Statement and signature. To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Name: Peter J. Yim Registration No: 44,417  Signature	 Date
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Total number of pages comprising cover sheet, attachments and document: 3

Mail documents to be recorded with required cover sheet information to:
Commissioner of Patents and Trademarks
Box Assignments
Washington, D.C. 20231

INTELLECTUAL PROPERTY ASSIGNMENT (PATENT)

For good and valuable consideration, the receipt of which is hereby acknowledged, DSP Semiconductors Ltd., currently known as DSP Group, Ltd. (the "Company"), an Israeli corporation having its registered offices at 5 Shenkar Street, Herzelia Pituach, 46120 ISRAEL, hereby assigns and transfers, effective as of the date of the signature on this document, to Corage, Ltd. (the "Assignee"), an Israeli corporation having its registered offices at 5 Shenkar Street, Herzelia Pituach, 46120 ISRAEL, all of such Company's right, title and interest in and to the patents and patent applications listed on the attached Exhibit A, including any patents issuing therefrom, divisional, continuation-in-part, or continuation applications, reissuances or extensions thereof and foreign counterparts thereto, all rights to sue at law or in equity for any past, present or future infringement thereof, and to receive all proceeds and damages therefrom, the same to be held and enjoyed by the Assignee, its successors and assigns, as fully as the same would have been held and enjoyed by the Company had this assignment not been made.

DSP SEMICONDUCTORS LTD.
currently known as DSP GROUP, LTD.

M. Zelnik

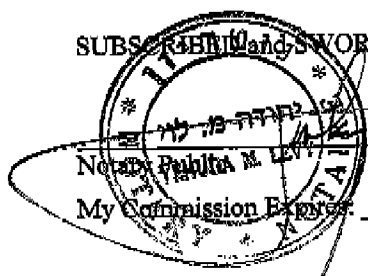
Signature

Moshe Zelnik
Vice President Finance

December 1, 2002

Date

SUBSCRIBED and SWORN to before me on this 24 day of December (Month/Year) 2002



My Commission Expires No Expiry (Month/Day/Year)

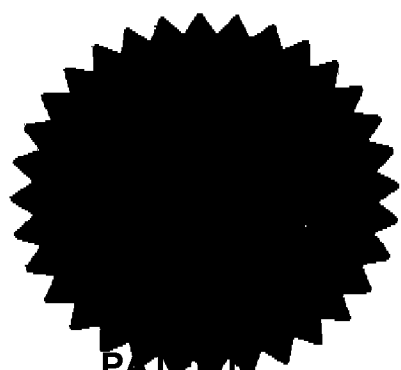


Exhibit A

U.S. Patent No.	Title of Invention
5,537,576	Expandable Memory for a Digital Signal Processor Including Mapped First and Second Memory Banks Forming a Continuous and Contiguous Address Space
5,463,749	Simplified Cyclical Buffer

BOSTON 1522379v2

RECORDED: 12/17/2002

PATENT
REEL: 13295 FRAME: 0788