	U.S. DEPARTMENT OF COMMERCE
	FORM COVER SHEET Patent and Trademark Office Docket No. 267450000044
To the Commissioner of Patents and Trademarks: P	ease record the attached original documents or copy thereof.
Name of conveying party(ics):     DSP Group, Ltd.	Name and address of receiving party(ics):      Name: Corage, Ltd.     Street Address: 5 Shenkar Street     City: Herzelia Pituach
Additional name(s) of conveying party(ies) attached? □Yes ■No  3. Nature of conveyance:	Country: ISRAEL ZIP: 46120
■ Assignment □ Merger □ Security Agreement □ Change of Name □ Other:  Execution Date: December 8, 2002	Additional name(s) & address(es) attached? 口 Yes 图 No
<ul> <li>4. Application number(s) or patent number(s):</li> <li>If this document is being filed together with a new application, to A. Patent Application No.(s)</li> <li>Additional numbers attached?  Yes  No (See Exhibit A)</li> </ul>	c execution date of the application is:  B. Patent No.(s) (See Exhibit A)
<ol> <li>Name and address of party to whom correspondence concerning document should be mailed:         <ul> <li>Peter J. Yim</li> <li>Morrison &amp; Foerster 112</li> <li>425 Market Street</li> <li>San Francisco, California 94105-2482</li> </ul> </li> </ol>	6. Total number of applications and patents involved: 17 7. Total fee (37 C.F.R. § 3.41): \$40.00  □ Enclosed ■ Authorized to be charged to deposit account, referencing Attorney Docket 267450000044  8. Deposit account number: 03-1952
The Commissioner is hereby authorized to charge any fees under 37 C.F.R. § 1.21 that m	ay be required by this paper, or to credit any overpayment to Deposit Account No. 03-1952.
DO NOT U	SE THIS SPACE
9. Statement and signature.  To the best of my knowledge and belief, the foregoing information document.  Name: Peter J. Yim Registration No: 44,417  Signature	on is true and correct and any attached copy is a true copy of the original
	cover sheet, attachments and document: 4
Commissioner of	Patents and Trademarks Assignment

# Exhibit A

Title of Invention
Dual Access Memory Array
Dual Access Memory Array
DSP for Two Clock Cycle Codebook Search
Method and Apparatus for Reducing the Penalty of Pipeline Breaks
Non Aligned Double Word Access in Word Addressable Memory
Scalar Product and Parity Check
Accumulation Saturation By Means of Feedback
Two Cycle FFT
Method for Reducing Code Size Using Programmable Delay Slots
Apparatus and Method for Decoding and Trace Back of Convolution Codes Using the Viterbi Decoding Algorithm
In-Place Memory Management for FFT
In-Place Memory Management for FFT
Complex Multiplication DSP Subarchitectures
Bit-Reversed Indexing in a Modified Harvard DSP Architecture
Code Generator for Viterbi Algorithm
Method and System for Setting a New Memory Address Pointer in a DSP
Split Barrel Shifter

**PATENT REEL: 13295 FRAME: 0790** 

#### INTELLECTUAL PROPERTY ASSIGNMENT (PATENT)

For good and valuable consideration, the receipt of which is hereby acknowledged, DSP Group, Ltd. (the "Company"), an Israeli corporation having its registered offices at 5 Shenkar Street, Herzelia Pituach, 46120 ISRAEL, hereby assigns and transfers, effective as of the date of the signature on this document, to Corage, Ltd. (the "Assignee"), an Israeli corporation having its registered offices at 5 Shenkar Street, Herzelia Pituach, 46120 ISRAEL, all of such Company's right, title and interest in and to the patents and patent applications listed on the attached Exhibit A, including any patents issuing therefrom, divisional, continuation-in-part, or continuation applications, reissuances or extensions thereof and foreign counterparts thereto, all rights to sue at law or in equity for any past, present or future infringement thereof, and to receive all proceeds and damages therefrom, the same to be held and enjoyed by the Assignee, its successors and assigns, as fully as the same would have been held and enjoyed by the Company had this assignment not been made.

DSP GROUP, LTD.

Signature

Moshe Zelnik

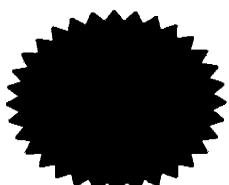
Vice President Finance

SUBSC

to before me on this 8th day of December 2002 (Month/Year)

No Expirey (Month/Day/Year)

BOSTON 1521789v2



REEL: 13295 FRAME: 0791

# Exhibit A

U.S. Patent/Application No.	Title of Invention
6,188,632	Dual Access Memory Array
6,407,961	Dual Access Memory Array
6,452,517	DSP for Two Clock Cycle Codebook Search
09/389,249	Method and Apparatus for Reducing the Penalty of Pipeline Breaks
09/379,142	Non Aligned Double Word Access in Word Addressable Memory
09/391,363	Scalar Product and Parity Check
09/391,823	Accumulation Saturation By Means of Feedback
09/587,617	Two Cycle FFT
09/392,641	Method for Reducing Code Size Using Programmable Delay Slots
09/406,788	Apparatus and Method for Decoding and Trace Back of Convolution Codes Using the Viterbi Decoding Algorithm
09/586,775	In-Place Memory Management for FFT
09/586,774	In-Place Memory Management for FFT
09/587,616	Complex Multiplication DSP Subarchitectures
10/028,938	Bit-Reversed Indexing in a Modified Harvard DSP Architecture
10/173,681	Code Generator for Viterbi Algorithm
09/385,205	Method and System for Setting a New Memory Address Pointer in a DSP
09/389,292	Split Barrel Shifter

BOSTON 1521789v2

**PATENT REEL: 13295 FRAME: 0792** 

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To: **USPTO Assignment Branch**  Facsimile: (703) 306-5995

From: Peter J. Yim

Date:

December 17, 2002

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Attorney Docket: 267450000044

Date: December 17, 2002

#### Papers Enclosed:

- 1. Recordation Cover Sheet w/copy of Exhibit A (2 pages)
  - 2. Assignment w/Exhibit A (2 page)
  - 3. This return receipt facsimile sheet.

PJY/jxh sf

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**PATENT** REEL: 13295 FRAME: 0793