

10-08-2002

FORM PTO-1595

(Rev. 03/01)

OMB No. 0651-0027 (exp. 05/31/2002)

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U.S. DEPARTMENT OF COMMERCE

U.S. Patent and Trademark Office

102244379

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

SILICON METRICS CORPORATION

10-3-02

2. Name and address of receiving party(ies):

Name: Silicon Valley Bank

Internal Address: Loan Documentation HA155

Street Address: 3003 Tasman Dr.

City: Santa Clara

State: Ca

ZIP: 95054

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

3. Nature of conveyance:

☐ Assignment☐ Merger☒ Security Agreement☐ Change of Name☐ Other _____

Execution Date: 9/16/02

Additional name(s) & address(es) attached? ☐ Yes ☒ No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: _____

A. Patent Application No.(s)

09/746,860 60/387,256

09/863,809 60/389,997

09/904,463 60/387,479

09/904,630

09/933,532

60/336,973

B. Patent No.(s)

6,327,557

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Silicon Valley Bank

Internal Address: Loan Documentation HA155

Street Address: 3003 Tasman Dr.

City: Santa Clara

State: Ca

ZIP: 95054

6. Total number of applications and patents involved: 10

7. Total fee (37 CFR 3.41):\$ 400.00

☒ Enclosed☐ Authorized to be charged to deposit account

8. Deposit account number:

(Attach duplicate copy of this page if paying by deposit account)

10/07/2002 DBYRNE 00000033 09746860

01 FC:581

400.00 DP

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

SHANNON HUBBARD

Name of Person Signing

Signature

9/24/02

Date

Total number of pages including cover sheets, attachments, and documents: 5

Mail documents to be recorded with required cover sheet information to:

Commissioner of Patents & Trademarks, Box Assignments
Washington, D.C. 20231PATENT
REEL: 013343 FRAME: 0656

ADDENDUM TO INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Addendum to Intellectual Property Security Agreement is executed pursuant to, and is an addendum to, an Intellectual Property Security Agreement, dated December 3, 1999, by and between Silicon Metrics Corporation ("Grantor") with its principal office at 12710 Research Blvd., Suite 300, Austin, Texas 78759, and Silicon Valley Bank ("Bank") with its principal office at 3003 Tasman Drive, Santa Clara, CA 95054. This Addendum to Intellectual Property Security Agreement is presented for recordation as constructive notice that Grantor, the owner of the intellectual property identified in the exhibit(s) attached hereto, has granted to Bank a security interest in the intellectual property described on exhibits(s) attached hereto, and the exclusive rights comprised in the intellectual property, to secure payment of a debt.

IN WITNESS WHEREOF, Grantor has executed this Addendum to Intellectual Property Security Agreement as of September 16, 2002.

GRANTOR:

SILICON METRICS CORPORATION


By: 
Name: SCOTT GORE
Title: CFO

EXHIBIT "A"
COPYRIGHTS

SCHEDULE A - ISSUED COPYRIGHTS

<u>COPYRIGHT DESCRIPTION</u>	<u>REGISTRATION NUMBER</u>	<u>DATE OF ISSUANCE</u>
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- None -

SCHEDULE B - PENDING COPYRIGHT APPLICATIONS

<u>COPYRIGHT DESCRIPTION</u>	<u>APPLICATION NUMBER</u>	<u>DATE OF FILING</u>	<u>DATE OF CREATION</u>	<u>FIRST DATE OF PUBLIC DISTRIBUTION</u>
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- None -

SCHEDULE C - UNREGISTERED COPYRIGHTS (Where No Copyright Application is Pending)

<u>DESCRIPTION</u>	<u>CREATION</u>	<u>DISTRIBUTION FROM ASSIGNOR</u>	<u>ASSIGNOR</u>
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- None -

EXHIBIT "B"
PATENTS

<u>Patent Number</u>	<u>Patent Name/Description</u>	<u>Grant Date</u>
US 6327557	METHOD AND SYSTEM FOR CREATING ELECTRONIC CIRCUITRY—OVERSAMPLING AND DATA REDUCTION	12/4/01
09/707,333	PLUG-INS	
09/707,757	INTERCONNECT MODEL COMPILER	
09/746,860	ENCRYPTED COMPILER	
09/768,037	APPLICATION PERSONALITY	
09/863,809	RAPID PARAMETER PASSING	
09/904,463	APPARATUS FOR OPTIMIZED CONSTRAINT CHARATERIZATION WITH DEGRADATION OPTIONS AND ASSOCIATED METHODS	
09/904,630	APPARATUS AND METHODS FOR CONSTRAINT CHARACTERIZATION WITH DEGRADATION OPTIONS	
09/933,532	APPARATUS AND METHODS FOR CHARACTERIZING ELECTRONIC CIRCUITS HAVING MULTIPLE POWER SUPPLIES	
60/336,973	APPARATUS AND METHODS FOR CIRCUIT CHARATERIZATION (provisional)	
60/387,256	TIMING ANALYSIS USING CELL AND INTERCONNECT WAVEFOORMS (provisional)	
60/389,997	CELL AND INTERCONNECT TIMING ANALYSIS USING WAVEFORMS (provisional)	
60/387,479	SIMULATION COMPILER (provisional)	

EXHIBIT "C"

TRADEMARKS

<u>TRADEMARK DESCRIPTION</u>	<u>COUNTRY</u>	<u>SERIAL NO.</u>	<u>REG. NO</u>	<u>STATUS</u>
Silicon Metrics and Design	US	75-681,580	2387329	Registered
Silicon Metrics	US	75-584,149	2353345	Registered
SiliconSmart	US	75-936,236		Allowed
CoreRater	US	75-610,438		Abandoned
IORater	US	75-517,319		Abandoned
SpicePilot	US	75-517,318		Abandoned
PCX	US	75-705,887		Allowed
BlockRater	US	75-705,794		Abandoned
PowerRater	US	75-517,317		Abandoned
MemRater	US	75-705,795		Revived
MacroRater	US	75-727,112		Abandoned
RealityCheck	US	75-610,439	2451595	Registered
CellRater	US	75-452,232	2489497	Registered