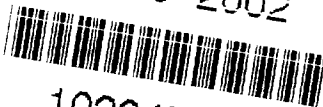


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Nakahara-ku, Kawasaki-shi
Kanagawa 211-8588 Japan
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Execution Date(s): September 13, 2002
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 This document is being filed together with a new application:
(a) The execution date(s) of the application is/are: September 13, 2002
(b) The title is: INTEGRATED LOGIC CIRCUIT AND HIERARCHICAL DESIGN METHOD THEREOF
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- Name and address of party to whom correspondence concerning document should be mailed:
STAAS & HALSEY LLP Our Docket: 1093.1052
Attention: David M. Pitcher
700 Eleventh Street, N.W., Suite 500
Washington, D.C. 20001
- Total number of applications and patents involved: 1
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David M. Pitcher, Reg. No. 25,908
Name of Person Signing

David M. Pitcher
Signature

October 3, 2002
Date

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IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter, "ASSIGNOR") by

(Insert Name(s) & Address(es) of ASSIGNEE(S))

FUJITSU LIMITED

1-1, Kamikodanaka 4-chome, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8588 Japan

(hereinafter, "ASSIGNEE"), the receipt of which is hereby acknowledged, the undersigned ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the entire and exclusive right, title and interest to the invention entitled

(Title of Invention)

INTEGRATED LOGIC CIRCUIT AND HIERARCHICAL DESIGN METHOD THEREOF

relating to International Patent Application PCT/JP_____/_____ and/or for which application for Letters Patent of the United States was executed on even date herewith or, if not so executed, was:

(a) executed on _____; (Insert date of execution of application, if not concurrent)

(b) filed on _____,
Serial No. ____/_____;

Any registered attorney of STAAS & HALSEY, 700 Eleventh Street, N.W., Washington, D.C. 20001 (202/434-1500) is hereby authorized to insert in (b) the specified data, when known.

and to said application and all Letters Patent(s) of the United States granted on said application and any continuation, division, renewal, substitute, reissue or reexamination application based thereon, for the full term or terms for which the said Letters Patent(s) may be granted and including any extensions thereof (collectively, hereinafter, "said application(s) and Letters Patent(s)").

The ASSIGNOR agree(s), when requested by said ASSIGNEE and without charge to but at the expense of said ASSIGNEE, to do all acts which the ASSIGNEE may deem necessary, desirable or expedient, for securing, maintaining and enforcing protection for said invention, including in the preparation and prosecution of said application(s) and the issuance of said Letters Patent(s), in any interference, reissue, reexamination, or public use proceeding, and in any litigation or other legal proceeding which may arise or be declared in relation to same, such acts to include but not be limited to executing all papers, including separate assignments and declarations, taking all rightful oaths, providing sworn testimony, and obtaining and producing evidence.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

(Typed Name & Signature of Inventor(s))

(Date)

(Typed Name & Signature of Witness(es))

1) <u>鈴木 賢司</u> Kenji Suzuki	<u>September 13, 2002</u>	<u>Yasuko Watanabe</u> Yasuko Watanabe
2) <u>森島 亨</u> Toru Osajima	<u>September 13, 2002</u>	<u>Yasuko Watanabe</u> Yasuko Watanabe
3) <u>田島 正吾</u> Shogo Tajima	<u>September 13, 2002</u>	<u>Yasuko Watanabe</u> Yasuko Watanabe
4) <u>佐藤 重伸</u> Shigenobu Satoh	<u>September 13, 2002</u>	<u>Yasuko Watanabe</u> Yasuko Watanabe
5) _____	_____	_____
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