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U.S. DEPARTMENT OF COMMERCE
U.S. Patent and Trademark Office

OMB No. 0651-0027 (exp. 5/31/2002)

102252084

Tab settings ⇨ ⇨ ⇨

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

MMC/GATX Partnership No. 1
Silicon Valley Bank

10-17-02

2. Name and address of receiving party(ies)

Name: Morphics Technology, Inc.

Internal Address: _____

Additional name(s) of conveying party(ies) attached? Yes No

3. Nature of conveyance:

- Assignment
- Merger
- Security Agreement
- Change of Name
- Other Termination of Security Interest

Street Address: 675 Campbell Technology Pkwy.

Suite 100

City: Campbell State: CA Zip: 95008

Execution Date: 9/30/02

Additional name(s) & address(es) attached? Yes No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: _____

A. Patent Application No.(s) _____
See attached.

B. Patent No.(s) _____
See attached.

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: MMC/GATX Partnership No. 1

Internal Address: c/o GATX Ventures, Inc.

Attn: Legal Department

Street Address: 3687 Mt. Diablo Blvd.

Suite 200

City: Lafayette State: CA Zip: 94549

6. Total number of applications and patents involved: 24

7. Total fee (37 CFR 3.41).....\$ 1040.00

Enclosed

Authorized to be charged to deposit account

8. Deposit account number:

DO NOT USE THIS SPACE

9. Signature.

MMC/GATX Partnership No. 1

Maureen C. Dellinger, Counsel
Name of Person Signing

Maureen C. Dellinger
Signature

10-10-02
Date

Total number of pages including cover sheet, attachments, and documents: 5

Mall documents to be recorded with required cover sheet information to:

Commissioner of Patents & Trademarks, Box Assignments
Washington, D. C. 20231

10/18/2002 6TON11 00000028 08662641

01 FC:8021

1040.00 DP

PATENT
REEL: 013376 FRAME: 0148

	Filing Date	Title	Filing Number
1	10-Jun-96	Method and apparatus for communicating information	08/662,641
2	10-Jun-96	Method and apparatus for communicating information	08/872,680
3	10-Jun-96	Method and apparatus for communicating information	08/872,974
4	07-May-99	Apparatus and method for dynamically defining variably sized autonomous sub-arrays within a gate array	60/133,138
5	07-May-99	Heterogeneous programmable gate array	60/133,145
6	07-May-99	Programmable data arithmetic array	60/133,134
7	07-May-99	Programmable logic device with a programmable security circuit	60/133,131
8	07-May-99	Apparatus and method for controlling a three-state bus	60/133,142
9	07-May-99	Apparatus and method for orthogonal bus transport	60/133,132
19	07-May-99	Apparatus and method for interleaved signal carrying in a programmable logic device	60/133,144
11	07-May-99	Apparatus and method for designing a circuit using minimum slice construction and replication	60/133,143
12	07-May-99	Apparatus and method for furcated mapping, placing, and routing of a programmable logic device	60/133,133
13	07-May-99	Reprogrammable processor architecture	60/133,141
14	07-May-99	Reprogrammable digital wireless communications device	60/133,137
15	07-May-99	Contact-switchable communications apparatus	60/133,129
16	07-May-99	Hardware-reconfigurable, software-programmable wireless communications device	60/133,135
17	07-May-99	Apparatus and method for profiling disparate communications and signal processing standards and services	60/133,130
18	07-May-99	Programmable digital intermediate frequency transceiver	60/133,136
19	07-May-99	Programmable broadband input/output processor	60/133,139
20	07-May-99	Apparatus and method for implementing a wireless system-on-a-chip with a reprogrammable tester, debugger, and bus monitor	60/133,140
21	30-Dec-99	Apparatus and method for generically generating code	-0029-888
22	30-Dec-99	Apparatus and method for generating a Fibonacci vector field from a Galois vector field	-0032-888
23	30-Dec-99	Apparatus and method for fast initial acquisition and searching within CDMA and WCDMA systems	-0033-888
24	30-Dec-99	Method and apparatus to support multistandard, multiservice base-stations for wireless voice and data networks	-0035-888
25	30-Dec-99	Apparatus and method for multimode digital despreading for CDMA signal processing	-0036-888
26	30-Dec-99	Apparatus and method for configurable all-digital coherent demodulation for WCDMA applications	-0037-888

**TERMINATION
OF
SECURITY INTEREST IN PATENTS**

This Termination of Security Interest in Patents (the "Termination") is executed on behalf of MMC/GATX Partnership No. I and Silicon Valley Bank, as Secured Parties (the "Secured Parties"), in favor of Morphics Technology, Inc., a Delaware corporation (the "Debtor"), and is effective as of September 30, 2002.

RECITALS

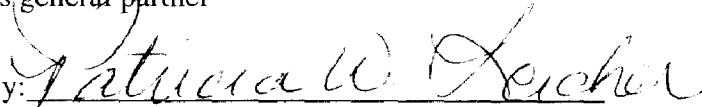
- A. Debtor and the Secured Parties entered into a certain Loan and Security Agreement, dated as of February 16, 2000 (the "Agreement").
- B. The Grant of Security Interest in Patents, relating to the Agreement, was filed with the Patent and Trademark Office on 4/10/2000, at Reel/Frame 010531/0931.
- C. Debtor has repaid all loan amounts due under the Agreement.

The Secured Parties therefore expressly terminate their security interest in the Collateral, including without limitation, the patents and patent applications listed on the schedule attached hereto.

IN WITNESS WHEREOF, this Termination is executed as of the date first above written.

MMC/GATX Partnership No. I

By: GATX Financial Corporation
(f/k/a GATX Capital Corporation),
its general partner

By: 

Name: Patricia W. Leicher
Title: Vice President

Silicon Valley Bank

By: _____
Name: _____
Title: _____

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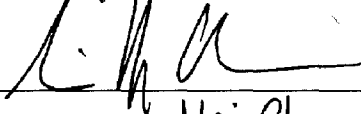
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MMC/GATX Partnership No. I

By: GATX Financial Corporation
(f/k/a GATX Capital Corporation),
its general partner

By: _____
Name: _____
Title: _____

Silicon Valley Bank

By:  _____
Name: Mei Chiu
Title: Vice President

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