

10-28-2002

Attorney Docket No. P-8309



102262809

EXPRESS MAIL LABEL NO.
EL 895673718 US

10/21/02

Form PTO-1595
(Rev. 03/01)
OMB No. 0651-0027 (exp. 5/31/2002)

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1. Name of conveying party(ies):
a) Upendra S. Brahme
b) Donald E. Fox
c)
d)
Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies):
Name: Sun Microsystems, Inc.
Internal Address:
Street Address: 4120 Network Circle
City: Santa Clara State: CA Zip: 95054
Additional name(s) & address(es) attached? Yes No

10/27/555
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3. Nature of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other
Execution Date:

4. Application number(s) or patent number(s): Attorney Docket No. P-8309
If this document is being filed with a new application, the execution date of the application is: 10/14/02 (Brahme) and 10/15/02 (Fox).
A. Patent Application No.(s) | B. Patent No.(s)
Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:
Name: Philip J. McKay
Gunnison, McKay & Hodgson, L.L.P.
Internal Address:
Street Address: Garden West Office Plaza
Suite 220
1900 Garden Road
City: Monterey State: CA Zip: 93940

6. Total no. of applications and patents involved: 1
7. Total fee (37 CFR 3.41) \$40.00
 Enclosed
 Authorized to charge any additional fees to deposit account
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To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.
Philip J. McKay [Signature] October 21, 2002
Name of Person Signing Signature Date

Total number of pages including cover sheet, attachments, and documents: 2

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ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, we,


Upendra S. Brahme of Fremont, California
Donald E. Fox of Milpitas, California

hereby sell, assign and transfer to Sun Microsystems, Inc., a Delaware corporation, having a place of business at 4120 Network Circle, Santa Clara, CA 95054 its successors and assigns, the entire right, title and interest throughout the world in our invention in

Method for Microprocessor Test Insertion Reduction

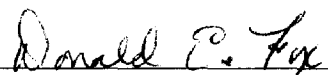
for which we have executed a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; we authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from me; we agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, its successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in all countries; and we request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Executed this 14th day of October, 2002.



Upendra S. Brahme
(Full Name)

Executed this 15th day of October, 2002.



Donald E. Fox
(Full Name)