10-30-2002

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This instrument confirms to the United States Government, as represented by the NATIONAL SCIENCE FOUNDATION, an irrevocable, nonexclusive, nontransferable, royalty-free license to practice or have practiced on its behalf throughout the world the following subject invention:

Title:

Method of Electroplating of High Aspect Ratio Metal

Structures into Semiconductors

Inventor(s):

Axel Scherer and Joyce Wong

Patent App Type: Nonprovisional

Serial No.:

09/699,117

Filing Date:

10/27/00

NSF Grant No:

ECS-9632937

The subject invention was made with the support of the above grant.

Principal rights to this subject invention have been left with the licensor.

Dated: March 2, 2001

CALIFORNIA INSTITUTE OF TECHNOLOGY

Adam Cochran

The Intellectual Property Counsel

Dated:

NATIONAL SCIENCE FOUNDATION

Accepted on behalf of NSF

CIT File No. 3114

RECORDED: 10/28/2002

PATENT REEL: 013423 FRAME: 0191