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		RECORD		U.S. Departr Patent and	nent of Commerce Trademark Office ATENT	
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Submission Type New	(Non-Recordation) TO Error Frame #		Conveyance To Assignment License Merger Security Agr Change of N	/pe eement	TO	
Conveying Party(i				Execution Date		
1. Chikao MAKITA 2. Hideki MIYAKE 3. 4. 5.	\	ying Parties Att	ached	October 3, 2002 October 3, 2002	11046 U.S. 10/27779	
	itsubishi Denki Kat	oushiki Kaisha				
Address C	-3, Marunouchi 2-cl hiyoda-ku okyo	home Japai	n	<u>100</u> -8310		
	ity		/Country	Zip Code		
Correspondent Na	al Names of Receiv	ing Parties Atta	ched			
Application Number(s) or Patent Number(s) 10/7 7			77799	Telephone: (202) 737-6770 Facsimile: (202) 737-6776 Attorney Docket No. 402313/SAKAI d conveyance document including any attachments: 2 7779		
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If this document is being filed together with a <i>new</i> Patent Application, enter the date the patent application was signed by the first named executing inventor. Patent Cooperation Treaty (PCT)					Month Day Year October 3, 2002	
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Number of Propert	Tes	-	Enter the	e total number of prop	erties involved: 1	
Fee Amount Fee Amount for Properties Listed (37 CFR 3.41): 40.00 Method of Payment: Enclosed is a check in the amount of \$ Charge Deposit Account No. 12-1216 Authorization to Charge Additional Fees to Deposit Account No. 12-1216: Yes No						
Statement and Signature To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Charges to deposit account are authorized, as indicated herein.						
Jeffrey A. Wyand		Affr	Megan	Ochherz	3, 2002	
Name of Pers	son Signing	<u> </u>	Signature/	D	ate	

Assignment Tml P1

PATENT REEL: 013445 FRAME: 0734

ASSIGNMENT

In consideration of the sum of One Dollar (\$1.00), and other good and valuable consideration, the receipt of which is acknowledged, the undersigned,

Chik	ao MAKITA and Hideki MIYAKE
2-3	Mitsubishi Denki Kabushiki Kaisha , Marunouchi 2-chome, Chiyoda-ku, TOKYO 100-8310 JAPAN assigns, and transfers to
Mits	ubishi Denki Kabushiki Kaisha
of 2-3	, Marunouchi 2-chome, Chiyoda-ku, TOKYO 100-8310 JAPAN
	ssignee), its successors and assigns, the entire right, and interest in and to:
(i)	the application for United States Letters Patent entitled:
_	LOW IMPEDANCE SEMICONDUCTOR INTEGRATED CIRCUIT
	which was executed by the undersigned on October 3, 2002 or was filed on and accorded Serial No (the Application);
(ii)	any and all inventions and improvements that are described in the Application;
(iii)	any and all United States Letters Patents that may be granted on the Application (the Patent);
(iv)	any and all divisions, continuations, reexaminations, reissues, or extensions of the Application or the Patent; and
(V)	all benefits under the International Convention for the Protection of Industrial Property.
I instru	authorize the Assignee and its agents to insert in this ment the filing date and serial number of the Application.
I any and	authorize and request the Commissioner of Patents to issued all Letters Patent, when granted, to the Assignee.

PATENT REEL: 013445 FRAME: 0735 Further, it is agreed that, when requested, without charge to, but at the expense of, the Assignee, the undersigned will execute all divisional, continuing, or reissue patent applications; execute all additional assignments and other writings, including reissue and reexamination papers; and provide all reasonable assistance requested by the Assignee to secure and maintain appropriate patent protection for any invention described in the Application.

INVENTOR	Date
Chikac Makita Chikao MAKITA	Oct. 3, 2002
Hileki MIYAKE	Oct. 3, 2002
	

RECORDED: 10/23/2002

PATENT REEL: 013445 FRAME: 0736